COURSE			YEAR OF			
CODE	COURSE NAME	L-T-P-C	INTRODUCTION			
EC301	Digital Signal Processing	3-1-0-4	2016			
Prerequisite: EC 202 Signals & Systems						

### **Course objectives:**

- 1. To provide an understanding of the principles, algorithms and applications of DSP
- 2. To study the design techniques for digital filters
- 3. To give an understanding of Multi-rate Signal Processing and its applications
- 4. To introduce the architecture of DSP processors

### Syllabus

Discrete Fourier Transform and its Properties, Linear Filtering methods based on the DFT, Frequency analysis of signals using the DFT, Computation of DFT, FFT Algorithms, IDFT computation using Radix-2 FFT Algorithms, Efficient computation of DFT of two real sequences and a 2N-Point real sequence, Design of FIR Filters, Design of linear phase FIR Filters using window methods and frequency sampling method, Design of IIR Digital Filters from Analog Filters, IIR Filter Design, Frequency Transformations, FIR Filter Structures, IIR Filter Structures, Introduction to TMS320C67xx digital signal processor, Multi-rate Digital Signal Processing, Finite word length effects in DSP systems, IIR digital filters, FFT algorithms.

### **Expected outcome:**

The students will understand

(i) the principle of digital signal processing and applications.

(ii) the utilization of DSP to electronics engineering

### **Text Books:**

- 1. Oppenheim A. V., Schafer R. W. and Buck J. R., Discrete Time Signal Processing, 3/e, Prentice Hall, 2007.
- 2. Proakis J. G. and Manolakis D. G., Digital Signal Processing, 4/e, Pearson Education, 2007.

### **References:**

- 1. Chassaing, Rulph., DSP applications using C and the TMS320C6x DSK. Vol. 13. John Wiley & Sons, 2003.
- 2. Ifeachor E.C. and Jervis B. W., Digital Signal Processing: A Practical Approach, 2/e, Pearson Education, 2009.
- 3. Lyons, Richard G., Understanding Digital Signal Processing, 3/e. Pearson Education India, 2004.
- 4. Mitra S. K., Digital Signal Processing: A Computer Based Approach, 4/e McGraw Hill (India), 2014.
- 5. NagoorKani, Digital Signal Processing, 2e, Mc Graw –Hill Education New Delhi, 2013
- 6. Salivahanan, Digital Signal Processing, 3e, Mc Graw –Hill Education New Delhi, 2014 (Smart book)
- 7. Singh A., Srinivasan S., Digital Signal Processing: Implementation Using DSP Microprocessors, Cenage Learning, 2012.

	Course Plan		<u>.</u>
Module	Course content	Hours	End Sem. Exam Marks
	The Discrete Fourier Transform: DFT as a linear transformation, Relationship of the DFT to other transforms, IDFT	2	
	Properties of DFT and examples Circular convolution	4	15
I	Linear Filtering methods based on the DFT- linear convolution using circular convolution, overlap save and overlap add methods	3	15
	Frequency Analysis of Signals using the DFT	2	
	Computation of DFT: Radix-2 Decimation in Time and Decimation in Frequency FFT Algorithms	3	
II	IDFT computation using Radix-2 FFT Algorithms	2	15
	Efficient computation of DFT of Two Real Sequences and a 2N-Point Real Sequence	2	
	FIRST INTERNAL EXAM		
	Design of FIR Filters- Symmetric and Anti-symmetric FIR Filters	2	
III	Design of linear phase FIR Filters using Window methods (rectangular, Hamming and Hanning) and frequency sampling Method	6	15
	Comparison of Design Methods for Linear Phase FIR Filters	1	
	Design of IIR Digital Filters from Analog Filters (Butterworth)	4	
IV	IIR Filter Design by Impulse Invariance, and Bilinear Transformation	3	15
	Frequency Transformations in the Analog and Digital Domain	2	
	SECOND INTERNAL EXAM		
	Block diagram and signal flow graph representations of filters	1	
	FIR Filter Structures: (Linear structures), Direct Form, Cascade Form and Lattice Structure	3	
V	IIR Filter Structures: Direct Form, Transposed Form, Cascade Form and Parallel Form	2	20
	Computational Complexity of Digital filter structures	1	
	Computer architecture for signal processing : Introduction to TMS320C67xx digital signal processor	2	
<b>X</b> 7 <b>T</b>	Multi-rate Digital Signal Processing: Decimation and Interpolation (Time domain and Frequency Domain Interpretation without proof)	3	20
VI	Finite word length effects in DSP systems: Introduction (analysis not required), fixed-point and floating-point DSP arithmetic, ADC quantization noise	2	- 20

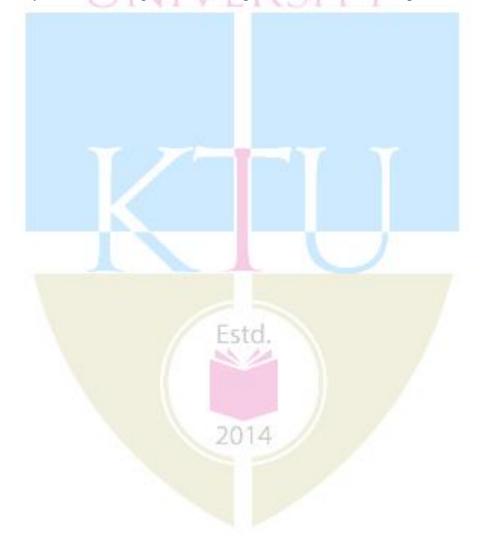
Finite word length effects in IIR digital filters: coefficient quantization errors	2		
Finite word length effects in FFT algorithms: Round off errors	2		
END SEMESTER EXAM			

### **Question Paper Pattern (End Sem Exam)**

### Maximum Marks: 100

# Time : 3 hours

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 40 % for theory and 60% for logical/numerical problems, derivation and proof.



	COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
	EC303	Applied Electromagnetic Theory	3-0-0-3	2016
Pr	erequisite: N	Vil		
Co	ourse objecti	ves:		
1.	To introduc	e basic mathematical concepts related to	electromagne	tic vector fields.
2.	To impart k	nowledge on the basic concepts of elect	ric and magnet	ic fields
3.	To develop	a solid foundation in the analysis and ap	plication of el	ectromagnetic fields,
	Maxwell's	equations and Poynting theorem.		
4.	To become	familiar with propagation of signal through	ugh transmissio	on lines and
	waveguides			
Sy	llabus:			
		nsformation, vector algebra, vector cal		-
		ations, Boundary condition, Solution of		
		different media, Poynting vector theor	em, transmiss	ion lines, Smith chart,
	aveguides.			
	spected outco			
		e course, students will be able:		
1.	-	a solid foundation and a fresh perspectiv	ve in the analys	sis and application of
_	electromagn			
2.	•	the propagation of electromagnetic wave		nedia.
3.	•	the characteristics of transmission lines.		
4. <i>-</i>		e different transmission line problems us	-	t
5.		nd the different modes of propagation in	n waveguides.	
	xt Books:			
1.		uus, Electromagnetics, 5/e, TMH, 2010.		· D ( 2014
2.		O Sadiku, Elements of Electromagnetics		
3.		, Jf Hayt, and John A. Buck. Engineerin	ig Electromagn	etics. McGraw-Hill,
<b>D</b>		v-Hill, 2014.		
	eferences:		- 1' C	DIII $2/-2012$
1.		Balmain, Electromagnetic waves and R	•••	
2.	-	dminister, Electromagnetics, Schaum's	Outline Series	McGraw HIII, 4/e,
3	1995 Martin A Pl	onus, Applied Electromagnetics, McG	$\sim_{\rm MW}$ Hill $\gamma_{\rm A}$ 10	78
3. 4.		<u>O. Sadiku &amp; S.V. Kulkarni</u> "'Principles		
т.		Press Inc. Sixth Edition, Asian Edition,2		
5.	•	ni Narayana Rao, Elements of Engineeri		netics Pearson 6/e
5.	2006.	in Furthy and Kuo, Elements of Englicent	ing Electroning	nonos, i ourson, 0/0,
6		on and Aziz S. Inon Engineering Fleet	company D	2010

6. Umran S. Inan and Aziz S. Inan, Engineering Electromagnetics, Pearson, 2010.

Course P			
Module	Course content	Hours	End Sem. Exam Marks
	Review of vector calculus, Spherical and Cylindrical coordinate system, Coordinate transformation	1	0
	Curl, Divergence, Gradient in spherical and cylindrical coordinate system.	1	U
	Electric field – Application of Coulomb's law, Gauss law and Amperes current law (proof not required, simple problems only)	1	
Ι	Poisson and Laplace equations (proof not required, simple problems only), Determination of E and V using Laplace equation.	1	15
	Derivation of capacitance and inductance of two wire transmission line and coaxial cable. Energy stored in Electric and Magnetic field.	2	15
	Displacement current density, continuity equation. Magnetic vector potential. Relation between scalar potential and vector potential.	2	
	Maxwell's equation from fundamental laws.	1	
	Boundary condition of electric field and magnetic field from Maxwell's equations	1	
II	Solution of wave equation	1	15
	Propagation of plane EM wave in perfect dielectric, lossy medium, good conductor, media-attenuation, phase velocity, group velocity, skin depth.	3	
	FIRST INTERNAL EXAM		
	Reflection and refraction of plane electromagnetic waves at boundaries for normal & oblique incidence (parallel and perpendicular polarization), Snell's law of refraction, Brewster angle.	4	
III	Power density of EM wave, Poynting vector theorem, Complex Poynting vector.	3	15
	Polarization of electromagnetic wave-linear, circular and elliptical polarisation.	2	
	Uniform lossless transmission line - line parameters	1	
IV	Transmission line equations, Voltage and Current distribution of a line terminated with load	2	15
	Reflection coefficient and VSWR. Derivation of input impedance of transmission line.	2	
	SECOND INTERNAL EXAM		
	Transmission line as circuit elements (L and C).	2	
$\mathbf{V}$	Half wave and quarter wave transmission lines.	1	20
v	Development of Smith chart - calculation of line impedance and VSWR using smith chart.	2	

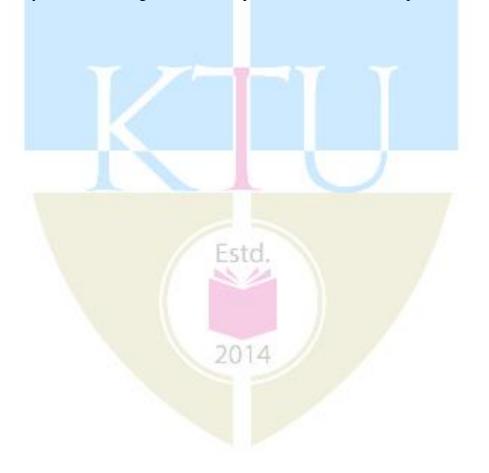
	Single stub matching (Smith chart and analytical method).	2	
	Parallel-Plate Waveguide - TE & TM waves.	1	
VI	The hollow rectangular wave guide – modes of propagation of wave- dominant mode, group velocity and phase velocity - derivation and simple problems only.	3	20
	Attenuation in wave guides, guide wavelength and impedance -derivation and simple problems only.	3	
END SEMESTER EXAM			

**Question Paper (End semester exam)** 

# Maximum marks : 100

# Time: 3 hours

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 50 % for theory and 50% for logical/numerical problems, derivation and proof.



COURSE			YEAR OF			
CODE	COURSE NAME	L-T-P-C	INTRODUCTION			
EC305	Microprocessor & Microcontroller	3-0-0-3	2016			
Davana and diates T	Provenue initial EC207 Leader Character Device					

# Prerequisite: EC207 Logic Circuit Design

### **Course objectives:**

- 1. To understand fundamental operating concepts of microprocessors and microcontrollers.
- 2. To communicate with various devices using controller.
- 3. To design a microcontroller based system with the help of the interfacing devices.
- 4. To program the controller to make various peripherals work for specified application.

# Syllabus:

Microprocessors: 8085 architecture and its operation, microprocessor initiated operations and bus organization, pin configuration and functions, generation of control signals for external operations- fetch, IO/M, read/write, machine cycles and bus timings. Addressing modes, instruction set, instruction classification. Overview/concept of peripheral IC interfacing with 8085 microprocessor (8251, 8253, 8255, 8279). Simple examples in assembly language programming for 8085 (only for internal examination). Introduction to development tools: IDE, cross assembler, builder, linker and debugger.( not required for exam). Introduction to 8086 and comparison between 8086, 80286, 80386, 80486 and Pentium.

Microcontrollers: 8051- features, architecture, memory organization, registers, I/O ports, pin configuration and functions. Addressing modes, instruction set, instruction classification. Assembly language programming. Interrupts in 8051. Timer/Counter programming: Operating modes, time delay generation, Waveform generation. Serial communication: RS 232 interface, registers in UART, modes of operation, programming examples for serial data transmission and reception. Interfacing of DIP switch, stepper motor, ADC, DAC, LEDs and seven segment displays, alphanumeric LCD module with 8051.

## **Expected outcome:**

The students will be able to:

- 1. Distinguish various types of processor architectures.
- 2. Describe architectures, memory organization of 8085 microprocessor and 8051.
- 3. Develop programming skills in assembly for interfacing peripheral devices with 8051

# **Text Books:**

- 1. Kenneth J. Ayala, The 8051 Microcontroller, Cengage learning, 3/e.
- 2. Lyla B.Das : Microprocessors and Microcontrollers, Pearson Education, India, 2011
- 3. Ramesh S. Goankar. 8085 Microprocessors Archiecture Application and Programming. Penram International, 5/e.

## **References:**

- 1. Aditya P Mathur, Introduction to Microprocessor. Tata Mc Graw Hill
- 2. Han Way Hung, "PIC Microcontroller, An introduction to software and hardware interfacing", Cenage learning.
- 3. I.Scott Mackenzie, Raphel C.-W Phan, The 8051 microcontroller, 4<sup>th</sup> edition.
- 4. Muhammed Ali Mazidi, The 8051 Microcontroller and Embedded Systems, Pearson Education, 2<sup>nd</sup> edition
- 5. Nagoorkani, Microprocessors and Microcontrollers 2e, McGraw Hill Education India, 2012.
- 6. Soumitra Kumar Mandal. Microprocessors and Microcontrollers Architecture, Programming & Interfacing Using 8085, 8086 and 8051, McGraw Hill Education (2011).
- 7.

	Course Plan		
Module	Course content	Hours	End Sem. Exam Marks
I	Microprocessors: Introduction, organization of a microprocessor based system, evolution of microprocessors, 8085 architecture and its operation, microprocessor initiated operations and bus organization, pin configuration and functions, generation of control signals for external operations-fetch, IO/M, read/write.	5	15
	Machine cycles and bus timings, Addressing modes, instruction set instruction classification. Overview/concept of peripheral IC interfacing with 8085	4	15
II	microprocessor (8251, 8253, 8255, 8279). Simple examples in assembly language programming for 8085	3	
	(only for internal examination) Introduction to development tools: IDE, cross assembler,	2	0
	builder, linker and debugger.( not required for exam) FIRST INTERNAL EXAM	_	
	Introduction to 8086 and comparison between		
	8086,80286,80386,80486 and Pentium	2	
III	Microcontrollers: Introduction, comparison between microprocessors and microcontrollers, microcontroller families, 8051- features, architecture, memory organization, registers, I/O ports, pin configuration and functions.	6	15
	Addressing modes, instruction set, instruction classification.	2	
IV	Assembly language programming examples for 8051.	3	15
	SECOND INTERNAL EXAM		
	Interrupts in 8051: Types, interrupt source, interrupt handling and programming	2	
V	Timer/Counter programming: Operating modes, time delay generation, Waveform generation.	2	20
	Serial communication: RS 232 interface, registers in UART, modes of operation, programming examples for serial data transmission and reception	2	
VI	Interfacing: Interfacing (block schematic and assembly language programming) of DIP switch, stepper motor, ADC, DAC, LEDs and seven segment displays, alphanumeric LCD module with 8051.	6	20
	END SEMESTER EXAM	-	-

# **Question Paper Pattern (End semester exam)**

# Max. Marks: 100

## Time: 3 hours

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 80 % for theory and 20% for logical/numerical problems and programming.

COURSE			YEAR OF			
CODE	COURSE NAME	L-T-P-C	INTRODUCTION			
EC307	Power Electronics & Instrumentation	3-0-0-3	2016			
	e: EC205 Electronic Circuits	0000	-010			
	Course objectives:					
v	de an insight on the concepts of Power Electror	nics and Ele	ctronic instruments.			
-	y the applications of Power electronics such a					
inverter			e			
3. To deve	lop understanding of the concept of Transducers	and Digital	instruments.			
Syllabus:						
Power semi	conductor switches and its static and dynamic	c characteris	tics. Switched mode			
regulators, S	SMPS, Switched mode inverters, UPS.					
Performanc	e characteristics of instruments, Measurement of	of passive co	omponents, Different			
Transducers	, Digital Instruments.					
Expected o						
	s will be able:					
	rstand the concepts of Power Electronics and the					
0	n insight on various electronic instruments, their	r configurati	on and			
	ments using them.					
	rstand the principle of operation of Transducers					
Text Books			1 II ' ' D			
	A., Electronic Instrumentation and Measuren	ments, Oxfo	rd University Press,			
2003.	A. H. "Dower Electronics Circuits Devices and	Application	s" Prontico Hall			
	M. H., "Power Electronics Circuits, Devices and nird Edition, New Delhi.	Application				
· · · ·	d L., Power Electronics Essentials and Applicat	ions Wiley	India 2015			
References		10110, W110 y				
	V. Hart, Power Electronics, McGraw Hill, 2011					
	E., Measurement Systems, 5/e, McGraw Hill, 2					
	A. D. and W. D. Cooper: Modern Electronic I		on and Measurement			
	Techniques, 5/e, PHI, 2003.					
	John Wiley, 2007.					
6. Nakra, I	nstrumentation, Measurement and Analysis,4e,	, Mc Graw -	-Hill Education New			
Delhi,20						
7. Patranal	bis D., Principles of Electronic Instrumentation,	PHI, 2008.				

	Course Plan		
Module	Course content	Hours	End Sem. Exam Marks
	Linear Electronics versus Power Electronics - Power semiconductor switches.	1	
	Power diodes-structure, static and dynamic characteristics	2	
Ι	Power transistors - Power BJT, Power MOSFET, GTO and IGBT	3	15
	Steady state and switching characteristics of Power BJT, Power MOSFET and IGBT.	2	
	Introduction to Switched mode regulators	1	
	Buck, Boost and Buck-Boost DC-DC converters	2	
Π	Waveforms and expression of DC-DC converters for output voltage, voltage and current ripple under continuous conduction mode. (Derivation not required)	1	15
	Isolated converters - Flyback, Forward, Push Pull, Half Bridge and Full Bridge Converters - waveforms and governing equations. (Derivation not required)	3	
	FIRST INTERNAL EXAM		
	Overview of SMPS, Switched mode inverters- Principles of	2	
	PWM switching schemes.		
III	Single phase inverters - half bridge, full bridge and push pull.	2	15
	UPS - on line and off line.	1	
	Three phase inverters - PWM and Space vector modulation in three phase inverters.	3	
	Generalized configurations of instruments - Functional elements. Classification of instruments	1	
IV	Generalized performance characteristics of instruments - Static characteristics and Dynamic characteristics.	2	15
	Measurement of: resistance using Wheastone's bridge, inductance using Maxwell-Wien bridge, and capacitance using Schering's bridge.	2	
	SECOND INTERNAL EXAM		
	Transducers - Classification, Selection of transducers.	1	
	Resistance transducers - Principle of operation, strain gauge.	2	
V	Inductive Transducers: LVDT.	2	20
	Capacitive transducers - different types, capacitor microphone, Hall Effect transducer, proximity transducers.	2	
	Electronic Multimeter, Audio Power Meter, RF power meter	2	
VI	Digital Instruments - Basics, digital measurement of time, phase, frequency and digital voltmeter.	2	20
	Frequency synthesizer, Spectrum analyzers, Logic State analyzers (block diagram only).	1	

Digital storage oscilloscope – Working Principle, controls and applications.	2		

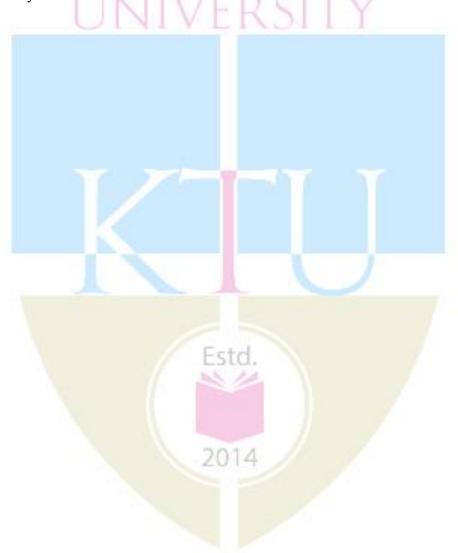
### END SEMESTER EXAM

# **Question Paper Pattern ( End Sem Exam)**

### Max. Marks: 100

### Time: 3 hours

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 100 % for theory.



Course cod	e Course Name	L-T-P - Credit		Year of roduction
HS300	Principles of Management	3-0-0-3		2016
Prerequisit	e : Nil	I		
Course Obj				
• To d	evelop ability to critically analyse and	evaluate a variety of mana	agement pr	actices in
the c	ontemporary context;			
	nderstand and apply a variety of mana			
	e able to mirror existing practices or to		ative mana	igement
-	betencies, required for today's complex	0 1		
	e able to critically reflect on ethical the	eories and social responsit	oility ideolo	ogies to
	e sustainable organisations.			
Syllabus Definition	also and functions of a management		and ant n	
	roles and functions of a manager, matched to challenges and the concepts like,			
	Early contributors and their contrib			
	consibility. Planning, Organizing,			
	Decision making under certainty,			-
0	nvolved in decision making.	, , , , , , , , , , , , , , , , , , ,	r	
Expected	0			
A student	who has undergone this course would l	be able to		
i.	manage people and organisations			
ii.	critically analyse and evaluate man	C 1	tices	
iii.	plan and make decisions for organiz			
iv.	do staffing and related HRD function	ons		
Text Book				ning 10th
Edition.	ontz and Heinz Weihrich, <i>Essentia<mark>ls</mark> o</i>	<i>f Management</i> , McGraw F	iiii Compa	nies, 10th
Reference	N•			
	. Daft, <i>New era Management</i> , 11th E	dition Cengage Learning	. /	
	. Griffin, Management Principles an			e Learning
	. Heinz Weirich, Mark V Cannice an			-
	Innovative and Entrepreneurial Pe			
2	Peter F Drucker, The Practice of M	anagement, McGraw Hill,	New York	K
4	. Robbins and Coulter, Management	, 13th Edition, 2016, Pears	on Educati	ion
	Cou	rse Plan		
Module	Contents	4	Hours	Sem. Exam Marks
	ntroduction to Managements definition	na managenial value and		
Introduction to Management: definitions, managerial roles and functions; Science or Art perspectives- External environment-				
I global, innovative and entrepreneurial perspectives of				
	Anagement (3 Hrs.)– Managing peo	1 1	6	
	he context of New Era- Managing for		0	
	he Challenges of Management (3 Hrs.			15%

	Early Contributions and Ethics in Management: Scientific			
п	Management- contributions of Taylor, Gilbreths, Human			
	Relations approach-contributions of Mayo, McGregor's			
	Theory, Ouchi's Theory Z (3 Hrs.) Systems Approach, the			
	Contingency Approach, the Mckinsey 7-S Framework			
	Corporate Social responsibility- Managerial Ethics. (3 Hrs)			
	Corporate Social responsionity- Managerial Eulies. (5 ms)		6	15%
	FIRST INTERNAL EXAMINATION			
	ADIARDIII KALAM	A		
III	Planning: Nature and importance of planning, -types of plans	V.I		
	(3 Hrs.)- Steps in planning, Levels of planning - The Planning	1	6	15%
	Process. – MBO (3 Hrs.).			
	Organising for decision making: Nature of organizing,			
	organization levels and span of control in management			
IV	Organisational design and structure –departmentation, line and			
	staff concepts (3 Hrs.) Limitations of decision making-			
	Evaluation and selecting from alternatives- programmed and		6	15%
	non programmed decisions - decision under certainty,			
	uncertainty and risk-creative process and innovation (3 Hrs.)			
	SECOND INTERNAL EXAMINATION			1
	Staffing and related HRD Functions: definition,			
	Empowerment, staff – delegation, decentralization and			
	recentralisation of authority – Effective Organizing and			
V	culture-responsive organizations –Global and entrepreneurial		0	2004
	organizing (3 Hrs.) Manager inventory chart-matching person		9	20%
	with the job-system approach to selection (3 Hrs.) Job design-			
	skills and personal characteristics needed in managers-			
	selection process, techniques and instruments (3 Hrs.)			
	Leading and Controlling: Leading Vs Managing – Trait approach and Contingency approaches to leadership -			
VI	Dimensions of Leadership (3 Hrs.) - Leadership Behavior and			
	styles – Transactional and Transformational Leadership (3			
	Hrs.) Basic control process- control as a feedback system –		9	20%
	Feed Forward Control – Requirements for effective control –		7	2070
	control techniques – Overall controls and preventive controls –			
	Global controlling (3 Hrs.)			
END SEMESTER EXAM				

**Question Paper Pattern** 

Max. marks: 100, Time: 3 hours. The question paper shall consist of three parts

Part A: 4 questions uniformly covering modules I and II. Each question carries 10 marks Students will have to answer any three questions out of 4 (3X10 marks =30 marks)
Part B: 4 questions uniformly covering modules III and IV. Each question carries 10 marks Students will have to answer any three questions out of 4 (3X10 marks =30 marks)
Part C: 6 questions uniformly covering modules V and VI. Each question carries 10 marks Students will have to answer any four questions out of 6 (4X10 marks =40 marks)

Note: In all parts, each question can have a maximum of four sub questions, if needed.

COURS					YEAR (	
CODE		RSE NAME	L-T-P-C	INT	RODUC	CTION
EC365		cal Engineering	3-0-0-3		2016	
Prerequisi						
Course ob						
		c biomedical engineerin				
	-	& physiology of major	systems of the b	ody ii	n design	ing
	ent for medical treat				61.	
		the principle and worki	ng of different ty	pes o	of <b>b</b> 10-m	edical
Syllabus:	ic equipment/device	5.				
	hy-overview Physic	ological systems of b	ody Messurem	ent (	of physi	ological
		peutic devices, Medical				
		lical imaging system	incontaiony equi	pmen	105, 1010	metry m
Expected of						
-	s will be able:					
		l therapy related equipn	nents.			
		and identify the neces		ent fo	or diagn	osis and
therapy						
	1	ce of electronics engine	•	field		
	-	ce of telemetry in patien	it care			
Text Book				~		
	1 7	of Biomedical instrumer				
		Weibell, Erich A. Pfeif	fer, Biomedical	Inst	rumental	tion and
References	ements, PHI, 2nd Ed	1tion, 2004				
		on to Biomedical Ins	trumentation (	amhr	idaa U	ivorsity
Press, 2		ion to biomedical ms	uumentation, C	amoi	luge Of	nversity
		iomedical Equipment T	echnology" Pe	arson	Educati	on 4 <sup>th</sup>
e/d.	.,	ionio ano an Indian bino in	, i e		200000	
	3. John G Webster, "Medical Instrumentation application and design", John Wiley 3 <sup>rd</sup> e/d.					
	4. Richard Aston, "Principle of Biomedical Instrumentation and Measurement". Merrill					
Education/Prentice Hall.						
<b>Course Pla</b>	n					
Module		<b>Course content</b>				End
					Hours	Sem.
					110015	Exam
						Marks
		medical instrumentation	•	iew	1	
		iological systems of the				
		ric potential: Resting a				
		ction potentials. Bio			2	15
		EEG, EMG, ERG,	EUG, EGG,	etc		15
	ntroduction only.)	mat valation				
	Electrode theory: Ne	rnst relation ctrodes: Microelectroc	lac clain and	-	1	
	electrodes, needle ele		les, skin surf	ace	1	
	nectiones, nectile ele					

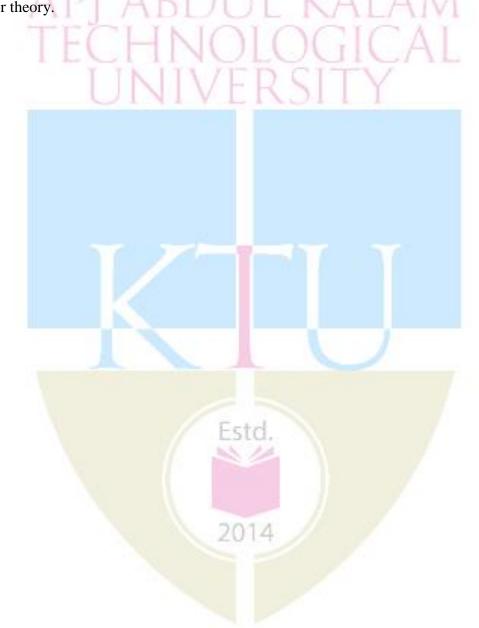
	Instrumentation for clinical laboratory: Bio potential amplifiers- instrumentation amplifiers, carrier amplifiers, isolation amplifiers, chopper amplifiers	2			
п	Heart and cardiovascular system (brief discussion), electro conduction system of the heart. Electrocardiography, ECG machine block diagram, ECG lead configurations, ECG recording system, Einthoven triangle, analysis of ECG signals.	3			
	Measurement of blood pressure: Direct, indirect and relative methods of blood pressure measurement, auscultatory method, oscillometric and ultrasonic non-invasive pressure measurements.	2	15		
	Measurement of blood flow: Electromagnetic blood flow meters and ultrasonic blood flow meters.	2			
	FIRST INTERNAL EXAM				
	The human nervous system. Neuron, action potential of brain, brain waves, types of electrodes, placement of electrodes, evoked potential, EEG recording, analysis of EEG.	2			
TTT	Electromyography: Nerve conduction velocity, instrumentation system for EMG.	1	15		
III	Physiology of respiratory system (brief discussion), Respiratory parameters, spirometer, body plethysmographs, gas exchange and distribution.	2	15		
	Instruments for clinical laboratory: Oxymeters, pH meter, blood cell counter, flame photometer, spectrophotometer	3			
IV	Therapeutic Equipments: Principle, block schematic diagram, working and applications of : pacemakers, cardiac defibrillators, heart–lung machine, dialyzers, surgical diathermy equipment, ventilators	6	15		
SECOND INTERNAL EXAM					
V	Medical Imaging systems (Basic Principle only): X-ray imaging - Properties and production of X-rays, X-ray machine, applications of X-rays in medicine.	2			
	Computed Tomograpy: Principle, image reconstruction, scanning system and applications.	2	20		
	Ultrasonic imaging systems: Basic pulse echo system, propagation of ultrasonic through tissues and reflections, display types, A-Scan, B-Scan, M-Scan, applications, real-time ultrasonic imaging systems and probes.	3			
VI	Magnetic Resonance Imaging – Basic NMR components, Biological effects and advantages of NMR imaging	3			
	Biomedical Telemetry system: Components of biotelemetry system, application of telemetry in medicine, single channel telemetry system for ECG and temperature	2	20		
	Patient Safety: Electric shock hazards, leakage current, safety codes for electro medical equipments	1			
	END SEMESTER EXAM				

### **Question Paper Pattern (End Sem. Exam)**

### Maximum Marks: 100

### Time : 3 hours

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 100 % for theory.



Course code	Course Name	L-T-P - Credits	Year of	
			Introduction	
**341	DESIGN PROJECT	0-1-2-2	2016	
Prerequisite : Nil				

### **Course Objectives**

- To understand the engineering aspects of design with reference to simple products
- To foster innovation in design of products, processes or systems
- To develop design that add value to products and solve technical problems

### **Course Plan**

**Study** :Take minimum three simple products, processes or techniques in the area of specialisation, study, analyse and present them. The analysis shall be focused on functionality, strength, material, manufacture/construction, quality, reliability, aesthetics, ergonomics, safety, maintenance, handling, sustainability, cost etc. whichever are applicable. Each student in the group has to present individually; choosing different products, processes or techniques.

**Design:** The project team shall identify an innovative product, process or technology and proceed with detailed design. At the end, the team has to document it properly and present and defend it. The design is expected to concentrate on functionality, design for strength is not expected.

*Note :* The one hour/week allotted for tutorial shall be used for discussions and presentations. The project team (not exceeding four) can be students from different branches, if the design problem is multidisciplinary.

#### **Expected outcome**.

The students will be able to

- i. Think innovatively on the development of components, products, processes or technologies in the engineering field
- ii. Analyse the problem requirements and arrive workable design solutions

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### **Reference:**

Michael Luchs, Scott Swan, Abbie Griffin, 2015. Design Thinking. 405 pages, John Wiley & Sons, Inc

### Evaluation

First evaluation (Immediately after first internal examination)20 marksSecond evaluation (Immediately after second internal examination)20 marksFinal evaluation (Last week of the semester)60 marks

*Note:* All the three evaluations are mandatory for course completion and for awarding the final grade.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC333	Digital Signal Processing Lab	0-0-3-1	2016
Prerequisit	ie:	•	
EC 213 Ele	ctronics Design Automation Lab, EC 20	2 Signals & Syst	ems
Course obj	ectives:		
• To e	enable the students to explore the conce	pts of design, sir	nulation and implementation
of v	arious systems using MATLAB/SciLab/	OCTAVE and D	SP kit.
List of Exp	eriments:	KA	IAM
	THI THEO	LIVI	LI LIVI
	xperiments on Digital Signal Processor	r/ DSP kits: (All	experiments are
mandatory		LUUI	N/L
1		DSIT	$\sim$
	Generation of sine wave and standard te	st signals.	1
	Convolution : Linear and Circular	TT'st	
	Real Time FIR Filter implementation (L inputting a signal from the signal genera	1 0 1	ass and Band-pass) by
	Real Time IIR Filter implementation (L		ass and Band nass) by
	inputting a signal from the signal genera		ass and Dand-pass) by
	Sampling of analog signal and study of a		
0.		inasing.	
Part B: Ex	periments based on MATLAB/SciLa	OCTAVE (7 e	xperiments are
mandatory	-		
•			
1.	Generation of Waveforms (Continuous a	and Discrete)	
	Verification of Sampling Theorem.		
	Time and Frequency Response of LTI sy		
	Linear Convolution, Circular Convolution	on and Linear Co	nvolution using Circular
	Convolution.		
	To find the DFT and IDFT for the given		
	Linear convolution using DFT (Overlap		
	To find the DCT and IDCT for the giver		
8. '	To find FFT and IFFT for the given input	it sequence	

- 8. To find FFT and IFFT for the given input sequence.
- FIR and IIR filter design using Filter Design Toolbox.
   FIR Filter (Low-pass, High-pass and Band-pass)design (Window method).
- 11. IIR Filter (Low-pass, High-pass and Band-pass)design (Hutterworth and Chebychev).
- 12. Generation of AM, FM & PWM waveforms and their spectrum.
- 13. Generation of DTMF signal. 20
- 14. Study of sampling rate conversion (Decimation, Interpolation, Rational factor).
- 15. Filtering of noisy signals
- 16. Implementation of simple algorithms in audio processing (delay, reverb, flange etc.).
- 17. Implementation of simple algorithms in image processing (detection, de-noising, filtering etc.)

# Expected outcome:

The students will be able to:

Design, simulate and realize various systems related to DSP.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION			
EC335	Power Electronics & Instrumentation Lab	0-0-3-1	2016			
Prerequisit	Prerequisite: NIL					
Course obj	ectives:					
• To d	esign and implement basic power electronic circuit	ts				
• To s	tudy the working of transducers					
• To ta	ain the usage of Digital Instruments					
List of Exp	eriments (8 experiments mandatory):					
	e I (Four mandatory)					
	esign and Set up DC-DC converter					
	esign and Set up Push pull DC- DC Converter					
	3. Design and Set up Buck DC-DC Converters					
	esign and Set up Simple SMPS					
	esign and Set up Half bridge and full bridge converte	rs				
6. De	esign and Set up basic Inverter Circuits					
Cycl	e II (Four mandatory)					
7. Tr	ansducer measurements using diode thermometer					
8. Tr	ansducer measurements using LVDT					
9. Tr	ansducer measurements using Strain gauge.					
10. 7	ransducer measurements using Pressure transducer.					
11. Т	11. Transducer measurements using Thermocouple & RTDS					
12. 7	ransducer measurements using Photocells					
Desi	red Experiment					
	tudy of Digital LCR meter, Frequency synthesizer, S	Spectrum analy	vzer and Logic State			
	/zer application.	r	,			
Expected or						
The students	s will be able to:					
1. Design and demonstrate basic power electronic circuits.						
2. Use	transducers for application.					

Use transducers for application.
 Function effectively as an individual and in a team to accomplish the given task.

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