



VIDYA ACADEMY OF SCIENCE & TECHNOLOGY TECHNICAL CAMPUS, KILIMANOOR, THIRUVANANTHAPURAM

“A Unit of Vidya International Charitable Trust”

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WEEKLY NEWS AND EVENTS

1st JULY 2026 to 31st JULY 2026

EVENTS/TALKS/SEMINARS/CONFERENCE

✓ ALUMNI CONTRIBUTION FROM THE 2015–19 BATCH

The **Department of Electronics and Communication Engineering** proudly acknowledges the valuable contribution made by its 2015–19 alumni batch towards the growth and development of the department on **5 July 2026**. As part of strengthening the bond between alumni and the institution, members of the 2015–19 batch visited the department and extended their generous support through an alumni contribution of 10,000 rupees.





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The contribution was formally handed over to the department in the presence of the Head of the Department, faculty members, and alumni representatives. The occasion reflected the alumni's continued commitment to giving back to their alma mater and supporting the academic and professional development of current students.

✓ CIVIL ENGINEERING DEPARTMENT CONDUCTS ONLINE PTA MEETINGS FOR S3, S5, AND S7 BATCHES

The **Department of Civil Engineering**, Vidya Academy of Science and Technology Technical Campus, Kilimanoor, successfully conducted Parent–Teacher Association (PTA) meetings for the S3, S5, and S7 batches through online mode. The meetings were held on **7 July 2026** (8:00–8:30 PM) for S3, **8 July 2026** (7:30–8:00 PM) for S5, and **9 July 2026** (8:00 PM onwards) for S7.

The meetings witnessed the active participation of almost all parents, along with the Head of the Department, class advisors, and all subject-handling faculty members. The Head of the Department welcomed the parents and emphasized the importance of fostering a strong partnership between the institution and parents. The respective class advisors presented an overview of the students' academic progress and overall performance.

The discussions focused on several important academic and disciplinary aspects, including the significance of Value-Added and Fast-Track (V-FAST) courses, university examination results, remedial and special classes for academically weaker students, student punctuality and discipline (wearing ID cards, adherence to the uniform code, and reducing late arrivals), internship opportunities, enrollment in NPTEL/MOOC online certification courses, and support classes for students with supplementary papers.

The PTA meetings concluded with an interactive session, during which parents shared valuable suggestions and appreciated the department's continuous efforts towards academic excellence and holistic development of the students.



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✓ DEPARTMENT TALK SESSION: TALK ON WELDING SIMULATION

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Molakal P.O., Kilimanoor, Thiruvananthapuram, Kerala

DEPARTMENT OF MECHANICAL ENGINEERING

**EXPERT TALK ON
WELDING
SIMULATION**

FEATURED SPEAKER:
Dr. R. Padmanaban
Associate Professor in Mechanical Engineering
Amrita School of Engineering, Coimbatore

DATE : JULY 15, 2026
TIME : 11:15 AM
Platform : Google Meet

COORDINATOR:
Dr. Subi.B
Associate Professor
Mechanical Engineering
VAST-TC

The **Department of Mechanical Engineering**, Vidya Academy of Science & Technology Technical Campus, Kilimanoor, successfully organized an Expert Talk on "**Welding Simulation**" on **15 July 2026** at 11:15 AM through Google Meet. The session was delivered by **Dr. R. Padmanaban**, Associate Professor in the Department of Mechanical Engineering at Amrita School of Engineering, Coimbatore. The expert talk focused on the importance of welding simulation in modern manufacturing industries, highlighting its role in predicting weld quality, minimizing defects, optimizing process parameters, analyzing residual stresses and distortions, and reducing product development time and cost.

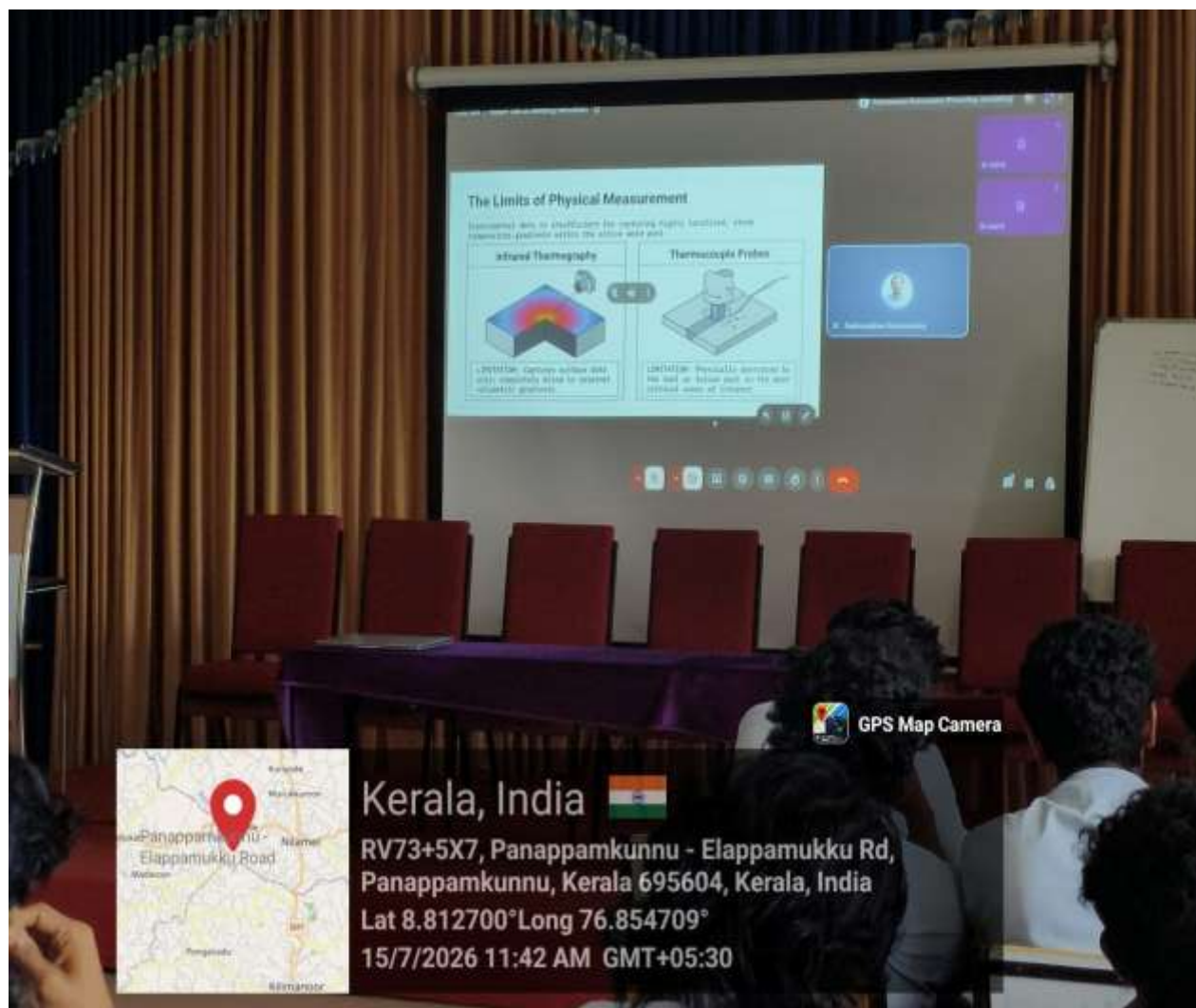


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The programme was coordinated by **Dr. Subi B**, Associate Professor, Department of Mechanical Engineering, VAST-TC. The expert talk concluded with a vote of thanks, expressing gratitude to the resource person for sharing his valuable knowledge and expertise. The session provided **70 participants** with a deeper understanding of welding simulation and its practical applications in engineering and manufacturing, enriching their academic and professional perspectives.





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✓ SCHOLARSHIP DISTRIBUTION FUNCTION 2026



Vidya Academy of Science & Technology Technical Campus (VAST TC), Kilimanoor, organized the **Scholarship Distribution Function 2026** on **17 July 2026** at the college lobby to recognize academic excellence and provide financial assistance to deserving students. The programme reaffirmed the institution's commitment to ensuring that financial constraints do not hinder students from pursuing quality technical education.

The function commenced with a prayer and was attended by members of the Vidya International Charitable Trust (VICT), management representatives, faculty members, parents, scholarship recipients, students and guests.

The gathering was welcomed by **Dr. Ansamma John, Principal, VAST TC**, who highlighted the institution's commitment to quality engineering education and its continued efforts to support deserving students through scholarships. She noted that the scholarship programme has become one of the institution's flagship initiatives, benefiting a large number of students every year.

The programme was presided over by **Sri. Gireesh Kumar, Chairman, Vidya International Charitable Trust (VICT)**. In his presidential address, he reiterated the Trust's commitment to



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promoting academic excellence through sustained scholarship support and encouraged students to make the best use of the opportunities provided to them.

The function was inaugurated by the **Chief Guest, Mr. Pradeep Gopal, Executive Director, Geo Chem Middle East, Dubai**. Drawing from his four decades of experience in the global oil and gas industry, he encouraged students to pursue excellence with determination, integrity and perseverance. He appreciated the efforts of Vidya International Charitable Trust in empowering students through financial assistance and urged the scholarship recipients to utilize the opportunity to build successful careers and contribute meaningfully to society.

Felicitation addresses were delivered by **Sri. Raju Karunakaran, Executive Member, VICT; Sri. Neelakandan Javaharial, Southern Regional Coordinator, VICT; Sri. Mukundan N., Scholarship Committee Convenor; Brig. K. S. Shaji, Director, VAST TC; Mr. Devarajan R, Project Director; and Sri. V. Prasad, PTA President**, who congratulated the scholarship recipients and appreciated the institution's continued scholarship initiatives. The event was also graced by the presence of **Sri. Sureshlal, Executive Director, VICT, Dr. Shalij P. R, Director (Academics), VICT**, along with several other trustees of the Vidya International Charitable Trust. The highlight of the programme was the distribution of scholarships to students admitted during the **2025–26 academic year**. Scholarships were awarded under the **Vidya Merit-cum-Means Full Fee Scholarship, Vidya Merit-cum-Means Half Fee Scholarship, Vidya Merit-cum-Means B.Tech Lateral Entry Full Fee Scholarship, Vidya Merit-cum-Means B.Tech Lateral Entry Half Fee Scholarship**, and the **Vidya–AICTE Fee Waiver Scheme**. Altogether, **40 students** received scholarships under these schemes.

The recipients of the **Vidya Merit-cum-Means Full Fee Scholarship** were **Surya Nandana S. (Civil Engineering); Adhish Vijayakumar, Abhin J. R. and Akash G. S. (Mechanical Engineering); and Akshayjith A. S., Vimal Ram L., Amal Raj S., Parvathy S. Kumar, Subin Raj S., Siva Suvi Kumar, Athvaja V. S., Anaka R. and Rohitha Rajesh (Computer Science & Engineering)**.



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The **Vidya Merit-cum-Means Half Fee Scholarship** was awarded to **Asna S., Azif J., Fathima Y. and Prardhana Pradeep** (Civil Engineering); **Aromal Krishna M.** (Mechanical Engineering); **Anantha Krishnas, Midhun M., Salmanul Haris D. S., Niveditha V. R. and Abijith M. S.** (Computer Science & Engineering); **Sreya A. S., Shredha A. D. and Parvathy B.** (Electronics & Communication Engineering); and **Vaishnav R.** (Electronics & Communication Engineering – Management Quota).

The **Vidya Merit-cum-Means B.Tech Lateral Entry Full Fee Scholarship** was awarded to **Akhil M. Ramesh** (Civil Engineering), while the **Vidya Merit-cum-Means B.Tech Lateral Entry Half Fee Scholarship** was presented to **Devapriya D. S.** (Civil Engineering) and **Nisha S. J.** (Computer Science & Engineering).

Under the **Vidya AICTE Fee Waiver Scheme**, scholarships were awarded to **Meera Goury A. S., Adithya L. D. and Fardaan Muhammed R. S.** (Civil Engineering); **Ananthu S., Neha B. Darsan, Hariharan S., Arjun S., Aswin V. and Diyana Parveen J. S.** (Computer Science & Engineering); and **Ganesh Senan B.** (Mechanical Engineering).

Following the scholarship distribution, prizes were presented to the winners of **STAP 2025–26 (Science and Technology Awareness Programme)** in recognition of their outstanding performance and innovative talents, reaffirming the institution's commitment to nurturing scientific curiosity among school students.

The Vote of Thanks was delivered by **Ms. Anitha Vijayan, Trustee Coordinator, VAST TC**, who expressed gratitude to the Chief Guest, Chairman, members of the management, representatives of the Vidya International Charitable Trust, faculty members, parents, students, media personnel and everyone who contributed to the successful conduct of the programme.

The function concluded with the National Anthem, marking the successful completion of an event that celebrated academic excellence and reaffirmed the commitment of **Vidya Academy of Science & Technology Technical Campus** and the **Vidya International Charitable Trust**



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towards promoting inclusive and equitable higher education through meaningful scholarship support.





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✓ FACULTY INDUCTION PROGRAMME

The Five-Day Faculty Induction Programme (FIP) on "**Achieving Academic Excellence: Best Practices & Innovative Teaching**" was successfully conducted from **18 June to 24 June 2026** at Vidya Academy of Science and Technology Technical Campus (VAST-TC), Kilimanoor. The programme was jointly organized by the Institution's Innovation Council (IIC) in association with the Internal Quality Assurance Cell (IQAC), with the primary objective of equipping faculty members with contemporary pedagogical practices, research competencies, institutional quality enhancement strategies, and professional development skills.

The programme was conceived as a comprehensive professional development initiative aimed at strengthening the academic, research, administrative, and mentoring capabilities of faculty members. Through a carefully designed series of expert sessions, participants were introduced to innovative teaching methodologies, research excellence, student-centred learning approaches, academic documentation, institutional governance, quality assurance practices, and leadership development. The programme also emphasized the importance of Outcome-Based Education



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(OBE), continuous quality improvement, and lifelong professional learning in achieving institutional excellence.

The programme was conducted under the leadership of **Dr. C. Brijilal Ruban**, Professor and Head, Department of Computer Science and Engineering, who served as the Chief Patron and provided continuous encouragement and support throughout the programme. The programme was coordinated by **Dr. Pravin Rose T**, Professor and Head, Department of Applied Science and Placement Manager. **Dr. Sargunan K**, Professor and Head, Department of Civil Engineering, served as the co-Coordinator, ensuring the smooth planning and execution of all sessions.

The organizing team comprised **Mr. Sajith Krishnan R** (Department of Mechanical Engineering), **Ms. Adithya G. S** and **Ms. Sree Lakshmi M** (Department of Applied Science), and **Ms. Adheena V. S** and **Ms. Nayana Gopan** (Department of Civil Engineering). Their dedicated efforts in programme planning, participant coordination, technical support, and session management contributed significantly to the successful conduct of the Faculty Induction Programme.

The Faculty Induction Programme featured an eminent panel of academicians, scientists, educational administrators, counsellors, and industry experts who shared their vast professional experience with the participants. Distinguished resource persons included **Padma Shri M. Chandradathan**, Former Director of Vikram Sarabhai Space Centre (VSSC); **Dr. B. Anil**, Former Principal, Government Engineering College, Barton Hill; **Dr. Shalij P. R.**, Director of Academics, VAST (Retd. Dean–Research, APJ Abdul Kalam Technological University); **Dr. Rijo Jacob Thomas**, Associate Professor, TKM College of Engineering; **Mr. Banarji Bhaskaran**, Founder and CEO, Onward Business Consulting and Training; **Mr. Deepak Sugathan**, Counsellor, VAST-TC; **Dr. Ansamma John**, Principal, VAST-TC; and **Dr. Joy Varghese V. M.**, Associate Professor, College of Engineering, Thiruvananthapuram.

The programme covered a wide range of themes essential for faculty development, including Research Excellence: Publications, Patents and Grants; Institutional Excellence and Quality Improvement; Building Positive Teacher–Student Relationships; V-FAST: Empowering Students



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for First-Time Success; The Art of Teaching: Creating Active Learners; Goal Setting and Professional Development Planning; Understanding Institutional Systems and Faculty Expectations; and Academic Documentation for Quality Enhancement. These sessions provided participants with practical knowledge and innovative strategies to enhance teaching effectiveness, strengthen the research culture, improve academic quality, and foster student success.

The programme witnessed enthusiastic participation from faculty members representing various departments of the institution. Throughout the five days, participants actively engaged in expert lectures, interactive discussions, reflective exercises, experience-sharing sessions, and collaborative learning activities. The Faculty Induction Programme served as an excellent platform for professional interaction, knowledge exchange, and capacity building, enabling faculty members to update their pedagogical practices, strengthen their research orientation and deepen their understanding of institutional quality assurance mechanisms.

Overall, the programme successfully fulfilled its objectives by promoting academic excellence, innovation in teaching and learning, research and professional development, institutional quality enhancement, and student-centred educational practices. The knowledge, perspectives, and practical insights gained during the programme are expected to significantly contribute to improving teaching effectiveness, research productivity, academic governance, and the overall quality of education at VAST Technical Campus.



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✓ CREOVATE 4.0 – SKILL DEVELOPMENT & INNOVATION WORKSHOP

IEEE | SB VAST TC

**DEPARTMENT OF
ELECTRONICS &
COMMUNICATION ENGINEERING**

IEEE SB VAST TC In Association With ELECTA
presents

CREOVATE 4.0

INNOVATE. DESIGN. ENGINEER. IMPACT.

ENGINEERING IDEAS. REAL-WORLD SOLUTIONS.
LIMITLESS POSSIBILITIES.

*Turning Ideas
Into Intelligent
Solutions*

THINK
Creative
Ideation

DESIGN
Smart
Solutions

BUILD
Prototype
& Develop

CONNECT
Communicate
& Integrate

IMPACT
Deliver for
Tomorrow

Participants
+2 Students

LIMITED SPOTS. ENDLESS POSSIBILITIES.

DATE
TUESDAY
30 JUNE
2026

TIME
9:00AM
TO
12:00PM

VENUE
VIDYA ACADEMY
OF SCIENCE AND
TECHNOLOGY
TECHNICAL CAMPUS
KILIMANOOR

Register Now!

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The **Department of Electronics and Communication Engineering**, in association with IEEE SB VAST TC and ELECTA, successfully organized **CREOVATE 4.0**, a technical skill development and innovation workshop on **30 June 2026** from 9:00 AM to 12:00 PM at Vidya Academy of Science and Technology Technical Campus, Kilimanoor.

The event was designed to encourage students to think creatively, develop engineering solutions, and gain practical exposure to modern technologies. With the theme "**Innovate. Design. Engineer. Impact**", CREOVATE 4.0 provided 12 participants with an opportunity to transform innovative ideas into intelligent solutions through collaborative learning and hands-on activities.

The workshop focused on the complete engineering design process, beginning with creative ideation, followed by smart solution design, prototype development, communication of ideas, and finally, delivery of impactful engineering solutions. Students actively engaged in brainstorming sessions, technical discussions, and prototype demonstrations that enhanced their understanding of engineering problem-solving methodologies.





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The event created an interactive learning environment where students explored practical applications of Electronics and Communication Engineering concepts in areas such as embedded systems, IoT, robotics, automation, and intelligent systems.

Faculty members and volunteers guided the participants throughout the sessions, encouraging innovation, critical thinking, and technical excellence. The workshop served as an excellent platform for students to enhance their creativity, technical confidence, and project development skills.

The workshop reinforced the department's commitment to promoting experiential learning, innovation, and industry-relevant technical skills among future engineers.

✓ FIFA FEST 2026 – INVICTAZZ



As part of the FIFA World Cup 2026 celebrations, the Vidya Academy of Science and Technology Technical Campus, Kilimanoor, successfully organized FIFA Fest 2026 – **INVICTAZZ**, a vibrant event celebrating the spirit of football and sportsmanship on **18 July 2026**.

The programme was inaugurated by **Dr. Ansamma John**, Principal VAST TC, who officially declared the event open by kicking the football, symbolizing the commencement of the festivities. Following the inauguration, all the Heads of Departments enthusiastically participated in the Football Kick Challenge, demonstrating their support for the event and encouraging student



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participation. The programme was coordinated by **Mr. Jijeesh Sekhar J. R, Assistant Professor, Physical Education.**

The celebrations continued with an energetic student flash mob, followed by an impressive Football Freestyle Show in which students showcased their football skills and creativity. The event also featured FIFA World Cup 2026 fan-based football tournaments, in which students represented and competed in support of their favourite World Cup nations, creating a festive and competitive atmosphere across the campus.

The event witnessed enthusiastic participation from students and faculty members, making INVICTAZZ – FIFA Fest 2026 a grand success while fostering teamwork, sportsmanship, and the excitement of the FIFA World Cup among the campus community.





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✓ INDUSTRY OF THE FUTURE: AN INTRODUCTION TO DIGITAL TWIN





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The **Department of Mechanical Engineering**, in association with the Institution's Innovation Council (IIC), Vidya Academy of Science & Technology Technical Campus, Kilimanoor, organized an Expert Talk on "**Industry of the Future: An Introduction to Digital Twins**" on **21 July 2026** through Google Meet. The session was delivered by **Mr. Viswajith S. Nair**, TCS Research Scholar, who shared valuable insights into the concept, applications, and future scope of Digital Twin technology in modern industries.

During the session, the speaker explained how Digital Twins create virtual replicas of physical systems to enable real-time monitoring, predictive maintenance, process optimization, and data-driven decision-making. He highlighted the growing importance of Digital Twin technology in the era of Industry 4.0, emphasizing its applications in manufacturing, smart factories, healthcare, aerospace, automotive, and infrastructure sectors.

The interactive session provided participants with a comprehensive understanding of emerging industrial technologies and their role in enhancing productivity, efficiency, and sustainability. Around 77 Students and 5 faculty members actively participated in the discussion, making the programme highly informative and engaging. The session concluded with an interactive Q&A, where the speaker addressed the queries of participants and encouraged students to explore research and career opportunities in Digital Twin technology and smart manufacturing.





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✓ DEPARTMENT OF CSE CONDUCTS SESSION ON CO-PO MAPPING

The **Department of Computer Science and Engineering** organized a faculty development session on **Course Outcome (CO)-Program Outcome (PO) Mapping** on **23 July 2026**, to strengthen outcome-based education practices and enhance the quality of academic planning. The session was handled by **Ms. Vindhya Vijayan**, Assistant Professor, Department of Computer Science and Engineering and IQAC member, who provided a comprehensive overview of the preparation and effective utilization of the CO-PO mapping sheet in accordance with academic and accreditation requirements.

The session focused on the systematic formulation of Course Outcomes, their alignment with Program Outcomes, appropriate mapping levels, and best practices for preparing CO-PO mapping sheets. Practical examples and interactive discussions enabled faculty members to gain a deeper understanding of the outcome-based education framework and its implementation in curriculum delivery and assessment.

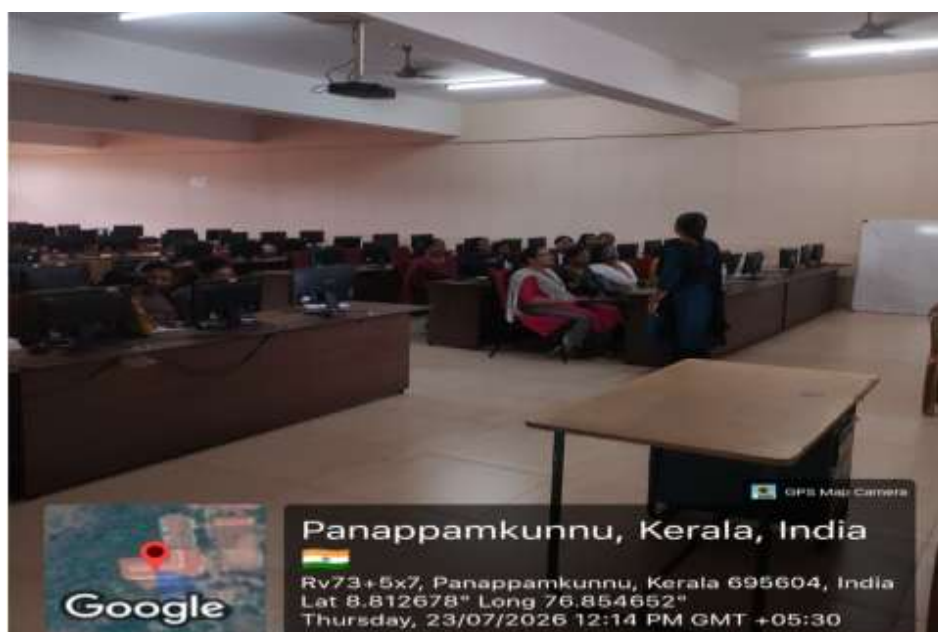
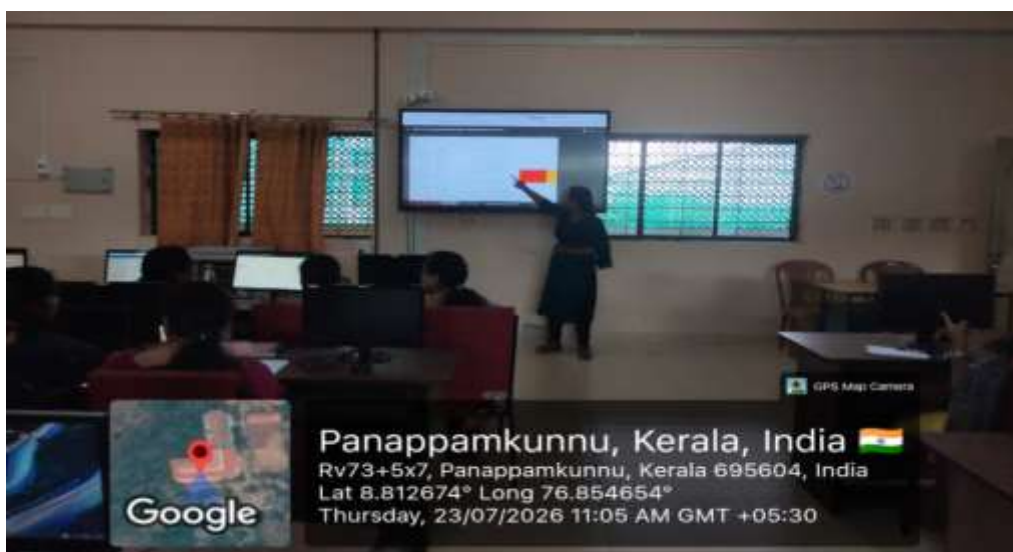


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All faculty members of the Department of Computer Science and Engineering actively participated in the session. The programme served as a valuable platform for enhancing faculty competency in outcome-based education and ensuring consistency in the preparation of CO-PO documentation, thereby contributing to continuous academic quality improvement.





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+ STUDENTS ACHIEVEMENTS

✓ S2 CSE A TOPPERS (2025-29 BATCH)

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TECHNICAL CAMPUS**

*** *Congratulations* ***

S2 TOPPERS
(2025-29 BATCH, CSE-A)

1 ANANTHU S SGPA - 9.05	2 ANSH KRISHNAN SGPA - 8.64	3 FARHAS RAHIM SGPA - 8.61	4 ASWIN V SGPA - 8.50	
5 DIYANA PARVEEN J S SGPA - 8.41	6 ARJUN S SGPA - 8.32	7 ARYA ASOK SGPA - 8.20	8 ARCHA S SGPA - 8.16	
9 ANAMIKA S SGPA - 7.89	10 DEVANARAYANAN S SGPA - 7.86	11 AMAL RAJ S SGPA - 7.59	12 ATHIRA RAJ B L SGPA - 7.50	13 DHYAN R NAIR SGPA - 7.50

Your hard work today, builds a brighter tomorrow.
★ **KEEP SHINING & REACHING NEW HEIGHTS!** ★



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✓ **S2 CSE B TOPPERS (2025-29 BATCH)**

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S2 B BATCH TOPPERS

★ CONGRATULATIONS! ★

Rank	Name	SGPA
1	VIMAL RAM L	9.0
2	NEHA B DARSAN	8.68
3	HARIHARAN S	8.48
4	SREYA S R	8.45
5	PARVATHY S KUMAR	7.95
6	SUMITHA K	7.68
7	MUHAMMAD ASHAR	7.59
8	VAISHNA G	7.59
9	MEENASHI K	7.52

★ YOUR HARD WORK, DEDICATION AND PERSEVERANCE HAVE TRULY SET YOU APART. ★

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✓ **S2 ECE TOPPERS (2025-29 BATCH)**

Department of Electronics and Communication Engineering
2025-29 BATCH ECE
S2 TOPPERS
Congratulations!!!

 SGPA 8.66 <i>Arjun Bija</i>	 SGPA 8.41 <i>Athmaja M S</i>
 SGPA 8.16 <i>Ganga U B</i>	 SGPA 8.11 <i>Anakha R</i>

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✓ S2 CE TOPPERS (2025-29 BATCH)

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CONGRATULATIONS !!!
S2 UNIVERSITY TOPPERS
2025 - 2029 Batch

 MEERA GOURI A S SGPA - 9.47	 ADITHYA L D SGPA - 9.13	 HANNA N ZAIN SGPA - 9.11	
 SURYA NANDANA SGPA - 8.97	 MUHAMMAD SHAMNAD S SGPA - 8.84	 SANDRA D S SGPA - 8.39	 PRARDHANA P SGPA - 8.26
 ASNA A S SGPA - 8.08	 FARDAAN MUHAMMED R S SGPA - 8.05	 AZIF J SGPA - 8.03	 DIYA S SGPA - 7.68

Department of Civil Engineering
"Crafting Structures, Creating Success Stories"



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✓ **S2 ME TOPPERS (2025-29 BATCH)**



✓ **S6 ECE TOPPERS (2023-27 BATCH)**





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✓ S6 CE TOPPERS (2023-27 BATCH)



✓ S6 ME TOPPERS (2023-27 BATCH)





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✓ **S6 CSE A TOPPERS (2023-27 BATCH)**

**DEPARTMENT OF COMPUTER
SCIENCE AND ENGINEERING**

S6 CSE A Toppers 2023 Batch

Congratulations!!!

 ANISHA A V SGPA: 9.24	 ALFIYA S SGPA: 9.22	 ANUSREE S SGPA: 8.98	 AKASH A S SGPA: 8.91
 DEVIKA DEEPU SGPA: 8.74	 ATHULYA U SGPA: 8.61	 FATHIMA RAMZAN T SGPA: 8.59	 AL SHANA NOUSHAD SGPA: 8.57
 ABHISHEK S R SGPA: 8.52	 ABHIJITH B S SGPA: 8.5	 ARSHA MOHAN SGPA: 8.41	 FEBA JAVASING SGPA: 8.41
 ALAN VARGHESE SGPA: 8.30	 DEVA PRIYA BYJU SGPA: 8.09	 DEVIKA V S SGPA: 8.04	 ATHUL S SGPA: 8.00

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✓ **PARTICIPATION IN F2F'26 STUDENT LEADERSHIP MEETING**



The **Department of Electronics and Communication Engineering** is pleased to announce the participation of **Ardra P** and **Charutha Chandran** from S7 ECE (2023-27 BATCH) in the F2F'26 Student Leadership Meeting, organized under the banner of IEEE SB VAST TC at Cochin. The meeting served as an excellent platform for student leaders to engage in meaningful discussions, exchange innovative ideas, and strengthen leadership, teamwork, and organizational skills. Through interactive sessions and collaborative activities, the participants gained valuable insights into effective student leadership, event management, and professional networking within the IEEE community. Their active participation reflects the department's commitment to fostering leadership qualities and encouraging students to take on impactful roles beyond academics. The Department congratulates Ardra P and Charutha Chandran on representing the institution and wishes them continued success in their future IEEE initiatives and leadership endeavours.



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✓ SUCCESSFUL COMPLETION OF THE 10-DAY EMBEDDED SYSTEMS AND IOT HANDS-ON WORKSHOP



The **Department of Electronics and Communication Engineering** successfully organized a 10-Day Hands-On Workshop on Embedded Systems and Internet of Things (IoT) for the S5 ECE (2024–28 Batch) students. The workshop was conducted from **22-06-2026** to **30-06-2026** by “**TEGAN GROUP OF COMPANY**” to bridge the gap between theoretical knowledge and practical implementation by providing students with extensive hands-on experience in embedded system design and IoT application development.

Throughout the ten-day programme, students were introduced to the fundamentals of embedded systems, microcontrollers, sensors, actuators, communication protocols, and IoT architectures. The sessions focused on practical learning, enabling participants to interface hardware components, develop embedded applications, and implement real-time IoT projects using development boards and software tools.



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The workshop included demonstrations, coding exercises, circuit implementation, debugging techniques, and mini project activities. Students gained practical exposure to sensor interfacing, wireless communication, data acquisition, cloud connectivity, and embedded programming. The interactive nature of the sessions encouraged teamwork, problem-solving, innovation, and critical thinking among the 20 participants.

The programme significantly enhanced the technical competencies of the students by strengthening their understanding of embedded technologies and their applications in smart systems. The workshop also motivated students to explore emerging technologies, undertake innovative projects, and prepare themselves for industry-oriented challenges in Electronics and Communication Engineering.

✓ S4 ECE (2024-28 BATCH) TOPPERS





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The **Department of Electronics and Communication Engineering** at Vidya Academy of Science & Technology, Technical Campus, Kilimanoor, proudly announces the S4 ECE Toppers of the 2024-28 Batch. Congratulate Ahmed N on securing the top position with an SGPA of 8.79, followed by Anas A with 8.47 and Vishnu H with 8.41. Their outstanding academic performance reflects dedication, hard work, and commitment. The department extends heartfelt congratulations to the toppers and wishes them continued success in their academic journey. The institution recognizes the outstanding academic performance of its students. Amritha P secured the top position with an SGPA of 7.96, followed by Abhiraj R with 7.95 SGPA, Adithyan U D with 7.64 SGPA, and Parvathi S with 7.61 SGPA. Their dedication and hard work reflect the department's commitment to building strong foundations for bright futures, and their achievement serves as an inspiration to all students in the department.

✓ S4 CSE A (2024-28 BATCH) TOPPERS





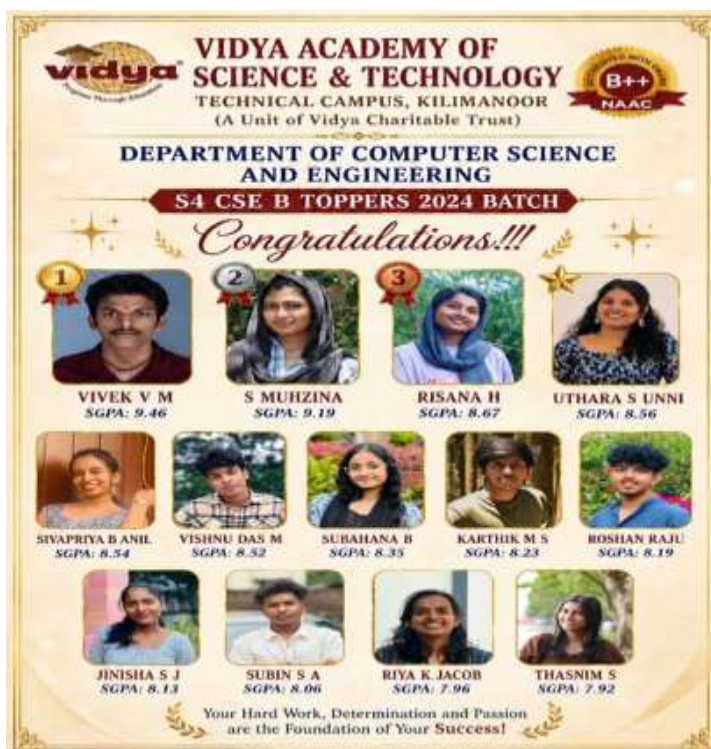
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The **Department of Computer Science & Engineering** at Vidya Academy of Science & Technology Technical Campus, Kilimanoor, proudly congratulates the S4 CSE-A Achievers of the 2024-28 Batch. The institution recognizes the outstanding academic performance of its students. Aryamol B secured the top position with an SGPA of 9.42, followed by Anjana A B with 9.4 SGPA, Afra Fathima R J with 8.54 SGPA, Aseem Jasmi with 8.52 SGPA, Athira P with 8.48 SGPA, Fathima A. S with 8.29 SGPA, Anjana A.B with 8.29 SGPA, Amna.N with 8.17 SGPA, Devamithra with 8.1 SGPA, Gourinandana. B with 7.92 SGPA, Fathima Siraja with 7.81 SGPA, and Farhana J S with 7.69 SGPA. Their dedication and hard work reflect the department’s commitment to building strong foundations for brilliant futures, and their achievement serves as an inspiration to all students in the department.

✓ S4 CSE-B (2024-28 BATCH) TOPPERS





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The **Department of Computer Science & Engineering** at Vidya Academy of Science & Technology Technical Campus, Kilimanoor, proudly congratulates the S4 CSE-B Achievers of the 2024-28 Batch. The institution recognizes the outstanding academic performance of its students. Vivek V M secured the top position with an SGPA of 9.46, followed by S Muhzina with 9.19 SGPA, Risana H with 8.67 SGPA, Uthara S Unni with 8.56 SGPA, Sivapriya B Anil with 8.54 SGPA, Vishnu Das M with 8.52 SGPA, Subahana B with 8.35 SGPA, Karthik M S with 8.23 SGPA, Roshan Raju with 8.19 SGPA, Jinisha S J with 8.13 SGPA, Subin S A with 8.06 SGPA, Riya K. Jacob with 7.96 SGPA, and Thasnim S with 7.92 SGPA. Their dedication and hard work reflect the department's commitment to building strong foundations for brilliant futures, and their achievement serves as an inspiration to all students in the department.

✓ **S4 CE (2024-28 BATCH) TOPPERS**





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The **Department of Civil Engineering** at Vidya Academy of Science & Technology Technical Campus, Kilimanoor, proudly congratulates the S4 University Toppers of the 2024–28 Batch for their commendable academic performance in the Fourth Semester University examinations. The department recognizes the hard work, dedication, and perseverance demonstrated by its students in achieving excellent results.

Brijili L secured the top position with an SGPA of 8.83, followed by **Akhil M Ramesh** with 8.67, **Nandana M** with 8.50, **Renjith R** with 7.96, **Arish Krishna** with 7.94, **Mahadev S** with 7.90, **Amiya A** with 7.81, **Devapriya D S** with 7.75, and **Sreelekshmi C** with 7.69.

The department extends its heartfelt congratulations to all the achievers for their outstanding accomplishments. Their consistent efforts, determination, and commitment to academic excellence reflect the quality of education and supportive learning environment fostered by the institution. The success of these students stands as a source of inspiration for their peers and reaffirms the department's commitment to nurturing competent, innovative, and socially responsible civil engineers. The management, Principal, faculty members, and fellow students wish the achievers continued success in all their future academic and professional endeavours.

✓ **S4 ME (2024-28 BATCH) TOPPERS**

The **Department of Mechanical Engineering** proudly congratulates the S4 B.Tech Mechanical Engineering students for their outstanding academic performance in the semester examinations. **Madhav James** secured the first position with an SGPA of 8.73, followed by **Mohammed Ashik S** with an SGPA of 8.63, and **Jayakrishnan A M** with an SGPA of 7.96. The department also appreciates the commendable performance of **Surya Sunil** (7.92), **Naveen K M** (7.69), **Mohammed Afsal S** (7.50), **Grace P Raju** (7.27), **Abhiram D** (7.17), and **Abhijith S** (7.04). The Department of Mechanical Engineering extends its heartfelt congratulations to all the achievers and wishes them continued success in their academic and professional endeavors.



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Marakkal P.O, Kilimanoor, Thiruvananthapuram, Kerala

DEPARTMENT OF MECHANICAL ENGINEERING
S4 ME TOPPERS
Congratulations

 MADHAV JAMES SGPA-8.73	 MOHAMMED ASHIK S SGPA-8.63	 JAYAKRISHNAN AM SGPA-7.96
 SURYA SUNIL SGPA-7.92	 NAVEEN K M SGPA-7.69	 MOHAMMED AFSAL S SGPA-7.50
 GRACE PRAJU SGPA-7.27	 ABHIRAM D SGPA-7.17	 ABHIJITH S SGPA-7.04

✓ S7 CSE A STUDENTS PUBLISH RESEARCH PAPER





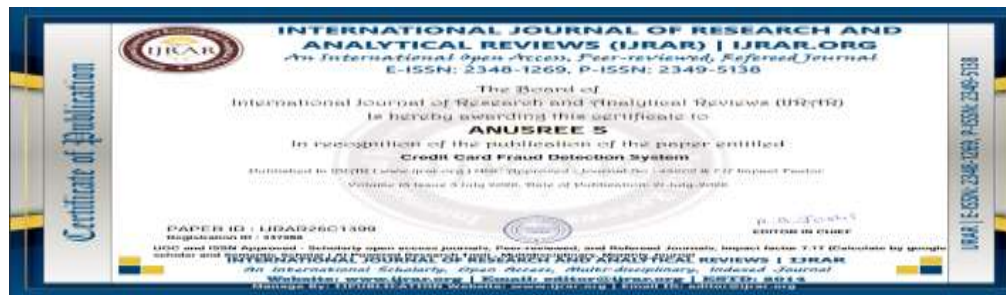
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Three teams of S7 Computer Science and Engineering students from Vidya Academy of Science and Technology Technical Campus, Kilimanoor, have successfully published their new research paper. The dedicated student teams include **Anusree S, Aswathy Vijayan, Anoop Panicker M S, and Adwaith Santhosh**. Their research, titled "**Credit Card Fraud Detection System**," was published in the International Journal of Research and Analytical Reviews (IJRAR). The paper was featured in Volume 13, Issue 3, and was officially published on **July 21, 2026**.

The research work was carried out under the expert guidance of **Ms. Anju M R**, Assistant Professor in the Department of Computer Science and Engineering. The paper presents a robust system for the crucial task of detecting and preventing credit card fraud. This publication highlights the students' active participation in research and innovation. It also strongly reflects the Department of Computer Science and Engineering's commitment to promoting research-driven learning and technological advancement.





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The second team of S7 Computer Science and Engineering students includes **Alfiya S**, **Adith V S**, **Akash A S** and **Aami Biju**. Their research, titled **"IP Address-based Smart Attendance System,"** was published in the International Journal of Research and Analytical Reviews (IJRAR). The paper was featured in Volume 13, Issue 3, and was officially published on **July 17, 2026**. The research work was carried out under the expert guidance of **Ms. Haritha P G**, Assistant Professor in the Department of Computer Science and Engineering. This publication highlights the students' active participation in research and innovation. It also strongly reflects the Department of Computer Science and Engineering's commitment to promoting research-driven learning and technological advancement.



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The third team of S7 Computer Science and Engineering students, **Ashish Jacob Shaiju, Arsha Mohan, Anisha A. V** and **Abhijith S**, has successfully published a research paper titled "**SignBridge AI: Real-Time Sign Language to Speech Conversion System**" in the International Journal of Engineering Research & Technology (IJERT), Volume 15, Issue 06, June 2026. The publication was officially registered on 2 July 2026 and received on **13 July 2026**.

The research work was carried out under the guidance of **Ms. Ashily M. Baby**, Assistant Professor, Department of Computer Science and Engineering. The paper presents an AI-powered real-time system that converts sign language into speech, aiming to bridge communication gaps for individuals with hearing and speech impairments.





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STAFF ACHIEVEMENTS

FDP PARTICIPATION



Ms. Anjana N, Faculty of the **Department of Electronics and Communication Engineering**, participated in two Six-Day Online Faculty Development Programmes (FDP). The first one, titled "**Advanced Communication Systems: Shaping Next Generation Networks**", was organized by the Department of Electronics and Communication Engineering, Jyothi Engineering College (Autonomous), Thrissur, Kerala, in association with The Institution of Engineers (India) (IEI),



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Kerala State Centre and the Indian Society for Technical Education (ISTE), held from 9 June to 16 June 2026. The programme focused on recent advancements in communication technologies, including next-generation wireless networks, 5G and beyond, IoT, software-defined networking, optical and satellite communication, artificial intelligence in communication systems, and emerging research trends. The Certificate Was Issued on **1st July 2026**.

The second FDP titled **"Transforming Higher Education: Innovative Pedagogies, AI Integration, and Global Best Practices"** was conducted from 29th June to 4th July 2026. The certificate was issued on **19th July 2026**.

The programme was jointly organized by Our Lady of Good Counsel Catholic University, Tirana, Albania, CHRIST (Deemed to be University), Bengaluru, and St. Joseph's College of Engineering and Technology, Palai, Kerala. Ms. Anjana's active participation reflects VASTTC's continued commitment to academic growth, innovation and preparing educators to meet the evolving needs of higher education.





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SUGGESTIONS & SUPPORT

We value your suggestions for improvement. For contact or assistance on related matters, please email us at wednesdaynews@vidyatcklmr.ac.in. We will get back to you as soon as possible.

Wednesday News Team