Course No.	Course Name	L-T-P - Credits	Year of Introduction
MA201	LINEAR ALGEBRA AND COMPLEX ANALYSIS	3-1-0-4	2016

Prerequisite : Nil

Course Objectives

COURSE OBJECTIVES

- To equip the students with methods of solving a general system of linear equations.
- To familiarize them with the concept of Eigen values and diagonalization of a matrix which have many applications in Engineering.
- To understand the basic theory of functions of a complex variable and conformal Transformations**.**

Syllabus

Analyticity of complex functions-Complex differentiation-Conformal mappings-Complex integration-System of linear equations-Eigen value problem

Expected outcome .

At the end of the course students will be able to
(i) solve any given system of linear equations
(ii) find the Eigen values of a matrix and how to diagonalize a matrix
(iii) identify analytic functions and Harmonic functions.
(iv)evaluate real definite Integrals as application of Residue Theorem
(v) identify conformal mappings(vi) find regions that are mapped under certain Transformations

Text Book:

Erwin Kreyszig: Advanced Engineering Mathematics, 10th ed. Wiley

References:

1.Dennis g Zill&Patric D Shanahan-A first Course in Complex Analysis with Applications-Jones&Bartlet Publishers
2.B. S. Grewal. Higher Engineering Mathematics, Khanna Publishers, New Delhi.
3.Lipschutz, Linear Algebra,3e **(** Schaums **Series)**McGraw Hill Education India 2005
4.Complex variables introduction and applications-second edition-Mark.J.Owitz-Cambridge Publication

Course Plan

Module	Contents	Hours	Sem. Exam Marks
I	Complex differentiation Text 1[13.3,13.4] Limit, continuity and derivative of complex functions	3	
	Analytic Functions	2	
	Cauchy–Riemann Equation(Proof of sufficient condition of analyticity & C R Equations in polar form not required)-Laplace's Equation	2	
	Harmonic functions, Harmonic Conjugate	2	15%
II	Conformal mapping: Text 1[17.1-17.4] Geometry of Analytic functions Conformal Mapping,	1	
	Mapping $w = z^2$ conformality of $w = e^z$.	2	15%

	The mapping $w = z + \frac{1}{z}$ Properties of $w = \frac{1}{z}$ Circles and straight lines, extended complex plane, fixed points Special linear fractional Transformations, Cross Ratio, Cross Ratio property-Mapping of disks and half planes Conformal mapping by $w = \sin z$ & $w = \cos z$ (Assignment: Application of analytic functions in Engineering)	1 3 3	
FIRST INTERNAL EXAMINATION			
III	Complex Integration. Text 1[14.1-14.4] [15.4&16.1] Definition Complex Line Integrals, First Evaluation Method, Second Evaluation Method Cauchy's Integral Theorem(without proof), Independence of path(without proof), Cauchy's Integral Theorem for Multiply Connected Domains (without proof) Cauchy's Integral Formula- Derivatives of Analytic Functions(without proof)Application of derivative of Analytical Functions Taylor and Maclaurin series(without proof), Power series as Taylor series, Practical methods(without proof) Laurent's series (without proof)	2 2 2 2 2	15%
IV	Residue Integration Text 1 [16.2-16.4] Singularities, Zeros, Poles, Essential singularity, Zeros of analytic functions Residue Integration Method, Formulas for Residues, Several singularities inside the contour Residue Theorem. Evaluation of Real Integrals (i) Integrals of rational functions of $\sin\theta$ and $\cos\theta$ (ii)Integrals of the type $\int_{-\infty}^{\infty} f(x)dx$ (Type I, Integrals from 0 to ∞) (Assignment : Application of Complex integration in Engineering)	2 4 3	15%
SECOND INTERNAL EXAMINATION			
V	Linear system of Equations Text 1(7.3-7.5) Linear systems of Equations, Coefficient Matrix, Augmented Matrix Gauss Elimination and back substitution, Elementary row operations, Row equivalent systems, Gauss elimination-Three possible cases, Row Echelon form and Information from it.	1 5	20%

	Linear independence-rank of a matrix Vector Space-Dimension-basis-vector space$\mathbf{R^3}$ Solution of linear systems, Fundamental theorem of non-homogeneous linear systems(Without proof)-Homogeneous linear systems (Theory only	2 1	
VI	Matrix Eigen value Problem Text 1.(8.1,8.3 &8.4) Determination of Eigen values and Eigen vectors-Eigen space Symmetric, Skew Symmetric and Orthogonal matrices –simple properties (without proof) Basis of Eigen vectors- Similar matrices Diagonalization of a matrix-Quadratic forms- Principal axis theorem(without proof) (Assignment-Some applications of Eigen values(8.2))	3 2 4	20%
END SEMESTER EXAM			

QUESTION PAPER PATTERN:

Maximum Marks : 100 Exam Duration: 3 hours

The question paper will consist of 3 parts.

Part A will have 3 questions of 15 marks each uniformly covering modules I and II. Each question may have two sub questions.

Part B will have 3 questions of 15 marks each uniformly covering modules III and IV. Each question may have two sub questions.

Part C will have 3 questions of 20 marks each uniformly covering modules V and VI. Each question may have three sub questions.

Any two questions from each part have to be answered.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC201	NETWORK THEORY	3-1-0-4	2016

Prerequisite: Nil

Course objectives:

- To make the students capable of analyzing any linear time invariant electrical network.
- To study time domain, phasor and Laplace transform methods of linear circuit analysis.
- To study the transient response of networks subject to test signals.
- To develop understanding of the concept of resonance, coupled circuits and two port networks.

Syllabus:

Circuit variables and Circuit elements, Kirchhoff's laws, Network topology, Mesh and node analysis of network, Laplace transform, Inverse Laplace transform, Solution of differential equations by using Laplace transforms, Transient analysis of RL, RC, and RLC networks, Network functions for the single port and two ports, Parameters of two-port network, Resonance, Coupled circuits

Expected outcome:

At the end of the course students will be able to analyze the linear time invariant electrical circuits.

Text Books

1. Ravish R., Network Analysis and Synthesis, 2/e, McGraw-Hill, 2015.
2. Valkenburg V., Network Analysis, 3/e, PHI, 2011.

References:

1. Sudhakar A,S. P. Shyammohan, Circuits and Networks- Analysis and Synthesis, 5/e, McGraw-Hill, 2015.
2. Choudhary R., Networks and Systems, 2/e, New Age International, 2013.
3. Franklin F. Kuo, Network Analysis and Synthesis, 2/e, Wiley India, 2012.
4. Pandey S. K., Fundamentals of Network Analysis and Synthesis, 1/e, S. Chand, 2012.
5. Edminister, Electric Circuits – Schaum's Outline Series, McGraw-Hill,2009.

Course Plan

Module	Course content (48 hrs)	Hours	Sem. Exam Marks (%)
I	Introduction to circuit variables and circuit elements ,Review of Kirchhoff's Laws, Independent and dependent Sources, Source transformations	3	15
	Network topology, Network graphs, Trees, Incidence matrix, Tie-set matrix and Cut-set matrix	2	
	Solution methods applied to dc and phasor circuits: Mesh and node analysis of network containing independent and dependent sources	3	
II	Network theorems applied to dc and phasor circuits: Thevenin's theorem, Norton's theorem, Superposition theorem, Reciprocity theorem, Millman's theorem, Maximum power transfer theorem	6	15

	Laplace transform, properties Laplace Transforms and inverse Laplace transform of common functions, Important theorems: Time shifting theorem, Frequency shifting theorem, Time differentiation theorem, Time integration theorem, s domain differentiation theorem, s domain integration theorem, Initial value theorem, Final value theorem	4	
	FIRST INTERNAL EXAM		
III	Partial Fraction expansions for inverse Laplace transforms, Solution of differential equations using Laplace transforms	3	**15**
	Transformation of basic signals and circuits into s-domain	2	
	Transient analysis of RL, RC, and RLC networks with impulse, step, pulse, exponential and sinusoidal inputs	3	
	Analysis of networks with transformed impedance and dependent sources.	3	
IV	Network functions for the single port and two ports, properties of driving point and transfer functions, Poles and Zeros of network functions, Significance of Poles and Zeros	3	**15**
	Time domain response from pole zero plot, Impulse Response	1	
	Network functions in the sinusoidal steady state, Magnitude and Phase response	3	
	SECOND INTERNAL EXAM		
V	Parameters of two port network: impedance, admittance, transmission and hybrid parameters, Interrelationship among parameter sets	5	**20**
	Series and parallel connections of two port networks	2	
	Reciprocal and Symmetrical two port network	2	
	Characteristic impedance, Image impedance and propagation constant (derivation not required)	2	
VI	Resonance: Series resonance, bandwidth, Q factor and Selectivity, Parallel resonance	3	**20**
	Coupled circuits: single tuned and double tuned circuits, dot convention, coefficient of coupling, Analysis of coupled circuits	4	
	END SEMESTER EXAM		

Question Paper Pattern (End Sem Exam)

Maximum Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark pattern is according to the syllabus with maximum 30% for theory and 70% for logical/numerical problems, derivation and proof.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC203	SOLID STATE DEVICES	3-1-0-4	2016

Prerequisite: Nil

Course objectives:

- To provide an insight into the basic semiconductor concepts
- To provide a sound understanding of current semiconductor devices and technology to appreciate its applications to electronics circuits and systems

Syllabus: Elemental and compound semiconductors, Fermi-Dirac distribution, Equilibrium and steady state conditions: Equilibrium concentration of electrons and holes, Temperature dependence of carrier concentration, Carrier transport in semiconductors, High field effects, Hall effect, Excess carriers in semiconductors , PN junctions ,contact potential, electrical field, potential and charge density at the junction, energy band diagram, minority carrier distribution, ideal diode equation, electron and hole component of current in forward biased pn junction, piecewise linear model of a diode , effect of temperature on VI characteristics, Diode capacitances, electrical breakdown in pn junctions, Tunnel Diode, Metal semiconductor contacts, bipolar junction transistor, metal insulator semiconductor devices, MOSFET, FinFET

Expected outcome:
The students should have a good knowledge in semiconductor theory and electronic devices.

Text Books:

1. Ben G. Streetman and Sanjay Kumar Banerjee, Solid State Electronic Devices, Pearson, 6/e, 2010
2. Achuthan, K N Bhat, Fundamentals of Semiconductor Devices, 1e, McGraw Hill,2015

References:

1. Tyagi M.S., Introduction to Semiconductor Materials and Devices, Wiley India, 5/e, 2008
2. Sze S.M., Physics of Semiconductor Devices, John Wiley, 3/e, 2005
3. Neamen, Semiconductor Physics and Devices, McGraw Hill, 4/e, 2012
4. Pierret, Semiconductor Devices Fundamentals, Pearson, 2006
5. Rita John, Solid State Devices, McGraw-Hill, 2014
6. Bhattacharya .Sharma, Solid State Electronic Devices, Oxford University Press, 2012
7. Dasgupta and Dasgupta , Semiconductor Devices : Modelling and Technology (PHI)

Course Plan

Module	Course content (48hrs)	Hours	Sem. Exam Marks
I	Elementalandcompoundsemiconductors,Fermi-Dirac distribution, Equilibrium and steady state conditions, Equilibrium concentration of electrons and holes, Temperature dependence of carrier concentration	4	15
	Carrier transport in semiconductors, drift, conductivity and mobility, variation of mobility with temperature and doping, High Field Effects, Hall effect	5	
II	Excess carriers in semiconductors: Generation and recombination mechanisms of excess carriers, quasi Fermi levels, diffusion, Einstein relations, Continuity equations, Diffusion length, Gradient of quasi Fermi level	9	15
	FIRST INTERNAL EXAM		

III	PN junctions : Contact potential, Electrical Field, Potential and Charge density at the junction, Energy band diagram, Minority carrier distribution, Ideal diode equation, Electron and hole component of current in forward biased p-n junction, piecewise linear model of a diode effect of temperature on V-I characteristics	9	**15**
IV	Diode capacitances, switching transients, Electrical Breakdown in PN junctions, Zener and avalanche break down (abrupt PN junctions only), Tunnel Diode basics only, Metal Semiconductor contacts, Ohmic and Rectifying Contacts, current voltage characteristics	9	**15**
SECOND INTERNAL EXAM			
V	Bipolar junction transistor , current components, Minority carrier distributions, basic parameters, Evaluation of terminal currents (based on physical dimensions),Transistor action, Base width modulation	9	**20**
VI	Metal Insulator semiconductor devices: The ideal MOS capacitor, band diagrams at equilibrium, accumulation, depletion and inversion, surface potential, CV characteristics, effects of real surfaces, work function difference, interface charge, threshold voltage MOSFET: Output characteristics, transfer characteristics, sub threshold characteristics, MOSFET scaling (basic concepts)	9	**20**
	FinFET- structure and operation	1	
END SEMESTER EXAM			

Question Paper Pattern (End Sem Exam)

Maximum Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark pattern is according to the syllabus with maximum 70 % for theory, derivation, proof and 30% for logical/numerical problems.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC205	ELECTRONIC CIRCUITS	3-1-0-4	2016

Prerequisite: Nil

Course objectives:

- To develop the skill of analysis and design of various analog circuits using discrete electronic devices as per the specifications.

Syllabus:

High pass and low pass RC circuits, Differentiator, Integrator, Analysis of BJT biasing circuits, small signal analysis of transistor configurations using small signal hybrid π model, low frequency and high frequency analysis of BJT amplifiers, Cascade amplifiers, Wide band amplifiers, Feedback amplifiers, Oscillators, Tuned amplifiers, Power amplifiers, Sweep circuits and multivibrators, transistor voltage regulator, DC analysis of MOSFET circuits, small signal equivalent circuit, Small signal analysis of MOSFET amplifier circuits, Analysis of multistage MOSFET amplifiers

Expected outcome:

- At the end of the course, students will be able to analyse and design the different electronic circuits using discrete electronic components.

Text Books:

• Sedra A. S. and K. C. Smith, Microelectronic Circuits, 6/e, Oxford University Press, 2013 • Millman J. and C. Halkias, Integrated Electronics, 2/e, McGraw-Hill, 2010

References:

1. Neamen D., Electronic Circuits - Analysis and Design, 3/e, TMH, 2007
2. Rashid M. H., Microelectronic Circuits - Analysis and Design, Cengage Learning, 2/e, 2011
3. Spencer R. R. and M. S. Ghausi, Introduction to Electronic Circuit Design, Pearson, 2003
4. Razavi B., Fundamentals of Microelectronics, Wiley, 2015

Course Plan

Module	Course content (48 hrs)	Hours	Sem. Exam Marks
I	RC Circuits: Response of high pass and low pass RC circuits to sine, step, pulse and square wave inputs, Differentiator, Integrator	5	15
	BJT biasing circuits: Types, Q point, Bias stability, Stability factors, RC coupled amplifier and effect of various components, Concept of DC and AC load lines, Fixing of operating point , Classification of amplifiers	5	
II	Small signal analysis of CE, CB and CC configurations using small signal hybrid π model (gain, input and output impedance). Small signal analysis of BJT amplifier circuits, Cascade amplifier	7	15
	FIRST INTERNAL EXAM		
III	High frequency equivalent circuits of BJT, Short circuit current gain, cutoff frequency, Miller effect, Analysis of high frequency response of CE, CB and CC amplifiers	4	15
	Wide band amplifier: Broad banding techniques, low frequency and high frequency compensation, Cascode amplifier.	4	
IV	Feedback amplifiers: Effect of positive and negative feedback on gain, frequency response and distortion, Feedback topologies and	3	15

	its effect on input and output impedance, Feedback amplifier circuits in each feedback topologies (no analysis required)		
	Oscillators & Tuned Amplifiers: Classification of oscillators, Barkhausen criterion, Analysis of RC phase shift and Wien bridge oscillators, Working of Hartley, Colpitts and Crystal oscillators; Tuned amplifiers, synchronous and stagger tuning	6	
SECOND INTERNAL EXAM			
V	Power amplifiers: Classification, Transformer coupled class A power amplifier, push pull class B and class AB power amplifiers, efficiency and distortion, Transformer-less class B and Class AB power amplifiers, Class C power amplifier (no analysis required)	6	**20**
	Switching Circuits: Simple sweep circuit, Bootstrap sweep circuit, Astable, Bistable, and Monostable multivibrators, Schmitt Trigger	5	
VI	Transistor based voltage regulator: Design and analysis of shunt and series voltage regulator, load and line regulation, Short circuit protection	4	**20**
	MOSFET amplifiers: Biasing of MOSFET amplifier, DC analysis of single stage MOSFET amplifier, small signal equivalent circuit. Small signal voltage and current gain, input and output impedances of CS configuration, MOSFETCascade amplifier	5	
END SEMESTER EXAM			

Question Paper Pattern (End Sem Exam)

Maximum Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark pattern is according to the syllabus with maximum 60 % for theory, derivation, proof and 40% for logical/numerical problems.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC207	LOGIC CIRCUIT DESIGN	3-0-0-3	2016

Prerequisite:Nil

Course objectives:

- To work with a positional number systems and numeric representations
- To introduce basic postulates of Boolean algebra and show the correlation between Boolean expression
- To outline the formal procedures for the analysis and design of combinational circuits and sequential circuits
- To study the fundamentals of HDL
- To design and implement combinational circuits using basic programmable blocks
- To design and implement synchronous sequential circuits

Syllabus:

Positional Number Systems, Boolean algebra, Combinational Logic, HDL concepts ,Digital ICs, Programmable Logic Devices, Sequential Logic, Sequential Circuits

Expected outcome:

The student should able to:

1. Compare various positional number systems and binary codes
2. Apply Boolean algebra in logic circuit design
3. Design combinational and sequential circuits
4. Design and implement digital systems using basic programmable blocks
5. Formulate various digital systems using HDL

Text Books:

1. Donald D Givone, Digital Principles and Design, Tata McGraw Hill, 2003
2. John F Wakerly, Digital Design Principles and Practices, Pearson Prentice Hall, 2007

References:

1.Ronald J Tocci, Digital Systems, Pearson Education, 11th edition,2010
2.Thomas L Floyd, Digital Fundamentals, Pearson Education, 8th edition 2009 3.Moris Mano, Digital Design, Prentice Hall of India, 3rd edition, 2002
4.John M Yarbrough, Digital Logic Applications and Design, Cenage learning, 2009
5.David Money Harris, Sarah L Harris, Digital Design and Computer Architecture, Morgan Kaufmann – Elsevier, 2009

Course Plan

Module	Course content (42 hrs)	Hours	Sem. Exam Marks
I	Number systems- decimal, binary, octal, hexa decimal, base conversion	2	15
	1's and 2's complement, signed number representation Binary arithmetic, binary subtraction using 2's complement	2	
	Binary codes (grey, BCD and Excess-3), Error detection and correcting codes : Parity(odd, even), Hamming code (7,4), Alphanumeric codes : ASCII	2	
II	Logic expressions, Boolean laws, Duality, De Morgan's law, Logic functions and gates	2	15
	Canonical forms: SOP, POS, Realisation of logic expressions using K-	2	

	map (2,3,4 variables)		
	Design of combinational circuits – adder, subtractor, 4 bit adder/subtractor, BCD adder, MUX, DEMUX, Decoder,BCD to 7 segment decoder, Encoder, Priority encoder, Comparator (2/3 bits)	4	
FIRST INTERNAL EXAM			
III	Introduction to HDL : Logic descriptions using HDL, basics of modeling (only for assignments)	**2**	**0**
	Logic families and its characteristics: Logic levels, propagation delay, fan in, fan out, noise immunity , power dissipation, TTL subfamilies	1	**15**
	NAND in TTL (totem pole, open collector and tri-state), CMOS:NAND, NOR, and NOT in CMOS, Comparison of logic families (TTL,ECL,CMOS) in terms of fan-in, fan-out, supply voltage, propagation delay, logic voltage and current levels, power dissipation and noise margin	2	
	Programmable Logic devices - ROM, PLA, PAL, implementation of simple circuits using PLA	2	
IV	Sequential circuits - latch, flip flop (SR, JK, T, D), master slave JK FF, conversion of FFs, excitation table and characteristic equations	3	**15**
	Asynchronous and synchronous counter design, mod N counters, random sequence generator	5	
SECOND INTERNAL EXAM			
V	Shift Registers - SIPO, SISO, PISO, PIPO, Shift registers with parallel LOAD/SHIFT Shift register counter - Ring Counter and Johnson Counter	3	**20**
	Mealy and Moore models, state machine ,notations, state diagram, state table, transition table, excitation table, state equations	3	
VI	Construction of state diagram – up down counter, sequence detector	3	**20**
	Synchronous sequential circuit design - State equivalence	2	
	State reduction – equivalence classes, implication chart	2	
END SEMESTER EXAM			

Assignments:

1. Simple combinational circuit design using MUX,DEMUX, PLA & PAL
2. HDL simulation of circuits like simple ALU, up-down counter, linear feedback shift register, sequence generator

Question Paper Pattern (End Sem Exam)

Maximum Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark pattern is according to the syllabus with maximum 50 % for theory, derivation, proof and 50% for logical/numerical problems.

Course code	Course Name	L-T-P - Credits	Year of Introduction
HS200	Business Economics	3-0-0-3	2016

Prerequisite: Nil

Course Objectives

- To familiarize the prospective engineers with elementary Principles of Economics and Business Economics.
- To acquaint the students with tools and techniques that are useful in their profession in Business Decision Making which will enhance their employability;
- To apply business analysis to the "firm" under different market conditions;
- To apply economic models to examine current economic scenario and evaluate policy options for addressing economic issues
- To gain understanding of some Macroeconomic concepts to improve their ability to understand the business climate;
- To prepare and analyse various business tools like balance sheet, cost benefit analysis and rate of returns at an elementary level

Syllabus

Business Economics - basic concepts, tools and analysis, scarcity and choices , resource allocation, marginal analysis, opportunity costs and production possibility curve. Fundamentals of microeconomics - Demand and Supply Analysis, equilibrium, elasticity, production and production function, cost analysis, break-even analysis and markets. Basics of macroeconomics - the circular flow models, national income analysis, inflation, trade cycles, money and credit, and monetary policy. Business decisions - investment analysis, Capital Budgeting decisions, forecasting techniques and elementary Balance Sheet and taxation, business financing, international investments

Expected outcome .

A student who has undergone this course would be able to

i. make investment decisions based on capital budgeting methods in alignment with microeconomic and macroeconomic theories.
ii. able to analyse the profitability of the firm, economy of operation, determination of price under various market situations with good grasp on the effect of trade cycles in business.
iii. gain knowledge on Monetary theory, measures by RBI in controlling interest rate and emerging concepts like Bit Coin.
iv. gain knowledge of elementary accounting concepts used for preparing balance sheet and interpretation of balance sheet

Text Books

1. Geetika, Piyali Ghosh and Chodhury, *Managerial Economics*, Tata McGraw Hill, 2015
2. Gregory Mankiw, *Principles of Macroeconomics*, Cengage Learning, 2006.
3. M.Kasi Reddy and S.Saraswathi, *Economics and Financial Accounting*. Prentice Hall of India. New Delhi.

References:

1. Dornbusch, Fischer and Startz, *Macroeconomics*, McGraw Hill, 11th edition, 2010.
2. Khan M Y, *Indian Financial System*, Tata McGraw Hill, 7th edition, 2011.
3. Samuelson, *Managerial Economics*, 6th edition, Wiley
4. Snyder C and Nicholson W, *Fundamentals of Microeconomics*, Cengage Learning (India), 2010.
5. Truett, Managerial Economics: Analysis, Problems, Cases, 8th Edition, Wiley
6. Welch, *Economics: Theory and Practice* 7th Edition, Wiley
7. Uma Kapila, *Indian Economy Since Independence, 26th Edition: A Comprehensive and Critical Analysis of India's Economy*, 1947-2015
8. C Rangarajan, *Indian Economy, Essays on monetary and finance*, UBS Publishers'Distributors, 1998
9. A.Ramachandra Aryasri, *Managerial Economics and Financial Analysis*, Tata McGraw-Hill, New Delhi.
10. Dominick Salvatore, *Managerial Economics in Global Economy*, Thomas Western College Publishing, Singapore.
11. I.M .Pandey, *Financial Management*, Vikas Publishing House. New Delhi.
12. Dominick Salvatore, *Theory and Problems of Micro Economic Theory.* Tata Mac Graw-Hill, New Delhi.
13. T.N.Hajela.*Money, Banking and Public Finance.* Anne Books. New Delhi.
14. G.S.Gupta. *Macro Economics-Theory and Applications.* Tata Mac Graw- Hill, New Delhi.
15. Yogesh, Maheswari, *Management Economics* , PHI learning, NewDelhi, 2012
16. Timothy Taylor , *Principles of Economics*, 3rd edition, TEXTBOOK MEDIA.
17. Varshney and Maheshwari. *Managerial Economics*. Sultan Chand. New Delhi

Course Plan

Module	Contents	Hours	Sem. Exam Marks
I	**Business Economics** and its role in managerial decision making-meaning-scope-relevance-economic problems-scarcity Vs choice (2 Hrs)-Basic concepts in economics-scarcity, choice, resource allocation- Trade-off-opportunity cost-marginal analysis- marginal utility theory, Law of diminishing marginal utility -production possibility curve (2 Hrs)	4	15%
II	**Basics of Micro Economics I** Demand and Supply analysis-equillibrium-elasticity (demand and supply) (3 Hrs.) -Production concepts-average product-marginal product-law of variable proportions- Production function-Cobb Douglas function-problems (3 Hrs.)	6	15%
FIRST INTERNAL EXAMINATION			
III	**Basics of Micro Economics II** Concept of costs-marginal, average, fixed, variable costs-cost curves-shut down point-long run and short run (3 Hrs.)- Break Even Analysis-Problem-Markets-Perfect Competition, Monopoly and Monopolistic Competition, Oligopoly-Cartel and collusion (3 Hrs.).	6	15%
IV	**Basics of Macro Economics** - Circular flow of income-two sector and multi-sector models- National Income Concepts-Measurement methods-problems-Inflation, deflation (4 Hrs.)-Trade cycles-Money-stock and flow concept-Quantity theory of money-Fischer's Equation and Cambridge Equation -velocity of circulation of money-credit control methods-SLR, CRR, Open Market Operations-Repo and Reverse Repo rate-emerging concepts in money-bit coin (4 Hrs.).	8	15%

SECOND INTERNAL EXAMINATION			
V	**Business Decisions I**-Investment analysis-Capital Budgeting-NPV, IRR, Profitability Index, ARR, Payback Period (5 Hrs.)- Business decisions under certainty-uncertainty-selection of alternatives-risk and sensitivity- cost benefit analysis-resource management (4 Hrs.).	9	20%
VI	**Business Decisions II** Balance sheet preparation-principles and interpretation-forecasting techniques (7 Hrs.)-business financing-sources of capital- Capital and money markets-international financing-FDI, FPI, FII-Basic Principles of taxation-direct tax, indirect tax-GST (2 hrs.).	9	20%
END SEMESTER EXAM			

Question Paper Pattern

Max. marks: 100, Time: 3 hours

The question paper shall consist of three parts

Part A
4 questions uniformly covering modules I and II. Each question carries 10 marks
Students will have to answer any three questions out of 4 (3X10 marks =30 marks)

Part B
4 questions uniformly covering modules III and IV. Each question carries 10 marks
Students will have to answer any three questions out of 4 (3X10 marks =30 marks)

Part C
6 questions uniformly covering modules V and VI. Each question carries 10 marks
Students will have to answer any four questions out of 6 (4X10 marks =40 marks)

Note: In all parts, each question can have a maximum of four sub questions, if needed.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC231	Electronic Devices & Circuits Lab	0-0-3-1	2016

Prerequisite: Should have registered for EC205 Electronic circuits

Course objectives:

- To study the working of analog electronic circuits.
- To design and implement analog circuits as per the specifications using discrete electronic components.

List of Experiments: (12 Mandatory Experiments)

1. VI Characteristics of rectifier and zener diodes
2. RC integrating and differentiating circuits (Transient analysis with different inputs and frequency response)
3. Clipping and clamping circuits (Transients and transfer characteristics)
4. Fullwave Rectifier -with and without filter- ripple factor and regulation
5. Simple Zener voltage regulator (load and line regulation)
6. Characteristics of BJT in CE configuration and evaluation of parameters
7. Characteristics of MOSFET in CS configuration and evaluation of parameters
8. RC coupled CE amplifier - frequency response characteristics
9. MOSFET amplifier (CS) - frequency response characteristics
10. Cascade amplifier – gain and frequency response
11. Cascode amplifier -frequency response
12. Feedback amplifiers (current series, voltage series) - gain and frequency response
13. Low frequency oscillators –RC phaseshift, Wien bridge,
14. High frequency oscillators –Colpitt's and Hartley
15. Power amplifiers (transformer less) - Class B and Class AB
16. Transistor series voltage regulator (load and line regulation)
17. Tuned amplifier - frequency response
18. Bootstrap sweep circuit
19. Multivibrators -Astable, Monostable and Bistable
20. Schmitt trigger

Expected outcome:

The student should able to:

1. Design and demonstrate functioning of various discrete analog circuits.
2. Function effectively as an individual and in a team to accomplish the given task.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC233	ELECTRONICS DESIGN AUTOMATION LAB	0-0-3-1	2016

Prerequisite: Nil

Course Objectives :

The primary objective of this course is to familiarize the students, how to simulate the electronics/digital circuits, signals and systems using the soft-wares which are available for the modern design methodologies for the rapid design and verification of complex electronic systems.

List of Exercises / Experiments

1. **Introduction to SPICE**

[Institution can use any one circuit simulation package with schematic entry like EDWinXP, PSpice, Multisim, Proteus or CircuitLab.]

Introduction to SPICE software. Recognize various schematic symbols /model parameters of resistor, capacitor, inductor, energy sources (VCVS, CCVS, Sinusoidal source, pulse, etc), transformer, DIODE, BJT, FET, MOSFET, etc., units & values. Use SPICE Schematic Editor to draw and analyse (DC, AC, Transient) simple analog and digital electronic circuits.

List of Experiments using SPICE [Six experiments mandatory]

Simulation of following circuits using SPICE [Schematic entry of circuits using standard package, Analysis –Transient, AC, DC]

1. Potential divider network
2. RC integrating and differentiating circuits
3. Diode, BJT and MOSFET characteristics
4. Diode Circuits (Clipping, Clamping, Rectifiers)
5. RC coupled amplifier (Single & two stages)
6. RC oscillator (RC phase shift / Wien Bridge)
7. Astable multivibrator
8. Truth table verification of basic and universal gates
9. Half adder /full adder circuits using gates
10. 4 bit adder/BCD adder
11. Encoder/Multiplexers
12. Flipflops/Counters

2. **Introduction to MATLAB**

[Institution can use any one numerical computational package like SciLab, Octave, Spyder, Python (scipy) or Freemat instead of MATLAB]

Fundamentals, basic operations on array, matrix, complex numbers etc., Script and function files, plotting commands, control statements.

Writing simple programs for handling arrays and plotting of mathematical functions, plotting of analog, discrete and noise signals, analysing the simple electronic circuits/network using node and mesh equations.

List of Experiments [Four experiments mandatory]

Write program and obtain the solutions

1. Solve /plot the mathematical equations containing complex numbers, array, matrix multiplication and quadratic equations etc

	2. Obtain different types of plots (2D/3D, surface plot, polar plot) 3. Generate and plot various signals like sine square, pulse in same window. 4. Plot the diode/transistor characteristics. 5. Solve node, mesh and loop equations of simple electrical/network circuits. 6. Find the poles and zeros hence plot the transfer functions/polynomials 7. Sort numbers in ascending order and save to another text file using text read and sort function after reading n floating point numbers from a formatted text file stored in the system. 8. Plot a full wave rectified waveform using Fourier series
3	**Introduction to HDL** [Institution can choose VHDL or Verilog as language to describe the problem and any one simulation/synthesis tool like Xilinix ISE, Modelsim, QSim, verilog, VHDL, EDwinXP or ORCAD etc. for the simulation.] **List of Experiments using HDL** Write the HDL code to realise and simulate the following circuits: (at least 4 of the following) 1. Basic gates/universal gates 2. Combinational Circuits (Half adder/Half subtractor) 3. Full adder in 3 modelling styles (Dataflow/structural/Behavioural) 4. Multiplexer/De-multiplexer 5. Decoder/Encoder 6. 4 bit adder/BCD adder 7. Flipflops (SR,JK,T,D) 8. Binary Counters 9. Finite state machines
Expected outcomes: 1. An ability to apply knowledge of computer, science, and engineering to the analysis of electrical and electronic engineering problems. 2. An ability to design systems which include hardware and software components. 3. An ability to identify, formulate and solve engineering problems. 4. An ability to use modern engineering techniques	